

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
A																									
B																									
C																									
D																									
E																									
F																									
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25

CUADRO DE
CONTROL
CALIDAD
AIRE

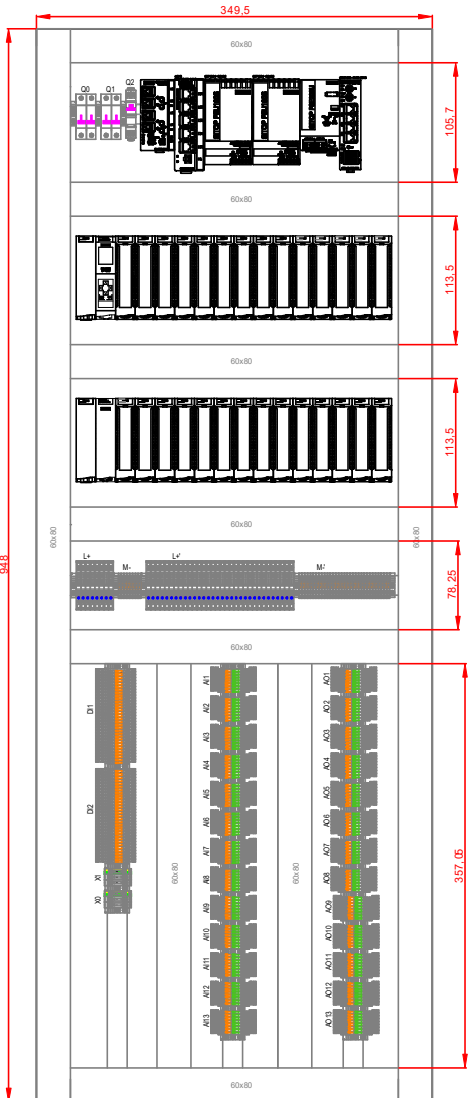
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	
A																										A
B																										B
C																										C
D																										D
E																										E
F																										F
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	

INDICE				
SL.NO.	DESCRIPCION	REV. NO.	REV	DESCRIPC FECHA
1	PORTADA	1	AS BUILT	AGD. 20
2	INDICE <1>	1	AS BUILT	AGD. 20
3	INDICE <2>	1	AS BUILT	AGD. 20
4	DISTRIBUCIÓN ARMARIO	1	AS BUILT	AGD. 20
5	ALIMENTACIONES 24VDC <1>	1	AS BUILT	AGD. 20
6	ALIMENTACIONES 24VDC <2>	1	AS BUILT	AGD. 20
7	ALIMENTACIONES 24VDC <3>	1	AS BUILT	AGD. 20
8	DI32xDC24 - RACK 1 - SLOT 2 <1> - BORNERO XDI1	1	AS BUILT	AGD. 20
9	DI32xDC24 - RACK 1 - SLOT 2 <2> - BORNERO XDI1	1	AS BUILT	AGD. 20
10	DI32xDC24 - RACK 2 - SLOT 2 <1> - BORNERO XDI2	1	AS BUILT	AGD. 20
11	DI32xDC24 - RACK 2 - SLOT 2 <2> - BORNERO XDI2	1	AS BUILT	AGD. 20
12	AI8x16BIT - RACK 1 - SLOT 3 - BORNERO XAI1	1	AS BUILT	AGD. 20
13	AI8x16BIT - RACK 1 - SLOT 4 - BORNERO XAI2	1	AS BUILT	AGD. 20
14	AI8x16BIT - RACK 1 - SLOT 5 - BORNERO XAI3	1	AS BUILT	AGD. 20
15	AI8x16BIT - RACK 1 - SLOT 6 - BORNERO XAI4	1	AS BUILT	AGD. 20
16	AI8x16BIT - RACK 1 - SLOT 7 - BORNERO XAI5	1	AS BUILT	AGD. 20
17	AI8x16BIT - RACK 1 - SLOT 8 - BORNERO XAI6	1	AS BUILT	AGD. 20
18	AI8x16BIT - RACK 1 - SLOT 9 - BORNERO XAI7	1	AS BUILT	AGD. 20
19	AI8x16BIT - RACK 1 - SLOT 10 - BORNERO XAI8	1	AS BUILT	AGD. 20
20	AI8x16BIT - RACK 1 - SLOT 11 - BORNERO XAI9	1	AS BUILT	AGD. 20
21	AI8x16BIT - RACK 1 - SLOT 12 - BORNERO XAI10	1	AS BUILT	AGD. 20
22	AI8x16BIT - RACK 1 - SLOT 13 - BORNERO XAI11	1	AS BUILT	AGD. 20
23	AI8x16BIT - RACK 1 - SLOT 14 - BORNERO XAI12	1	AS BUILT	AGD. 20
24	AI8x16BIT - RACK 1 - SLOT 15 - BORNERO XAI13	1	AS BUILT	AGD. 20

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	
A																										A
B																										B
C																										C
D																										D
E																										E
F																										F
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	

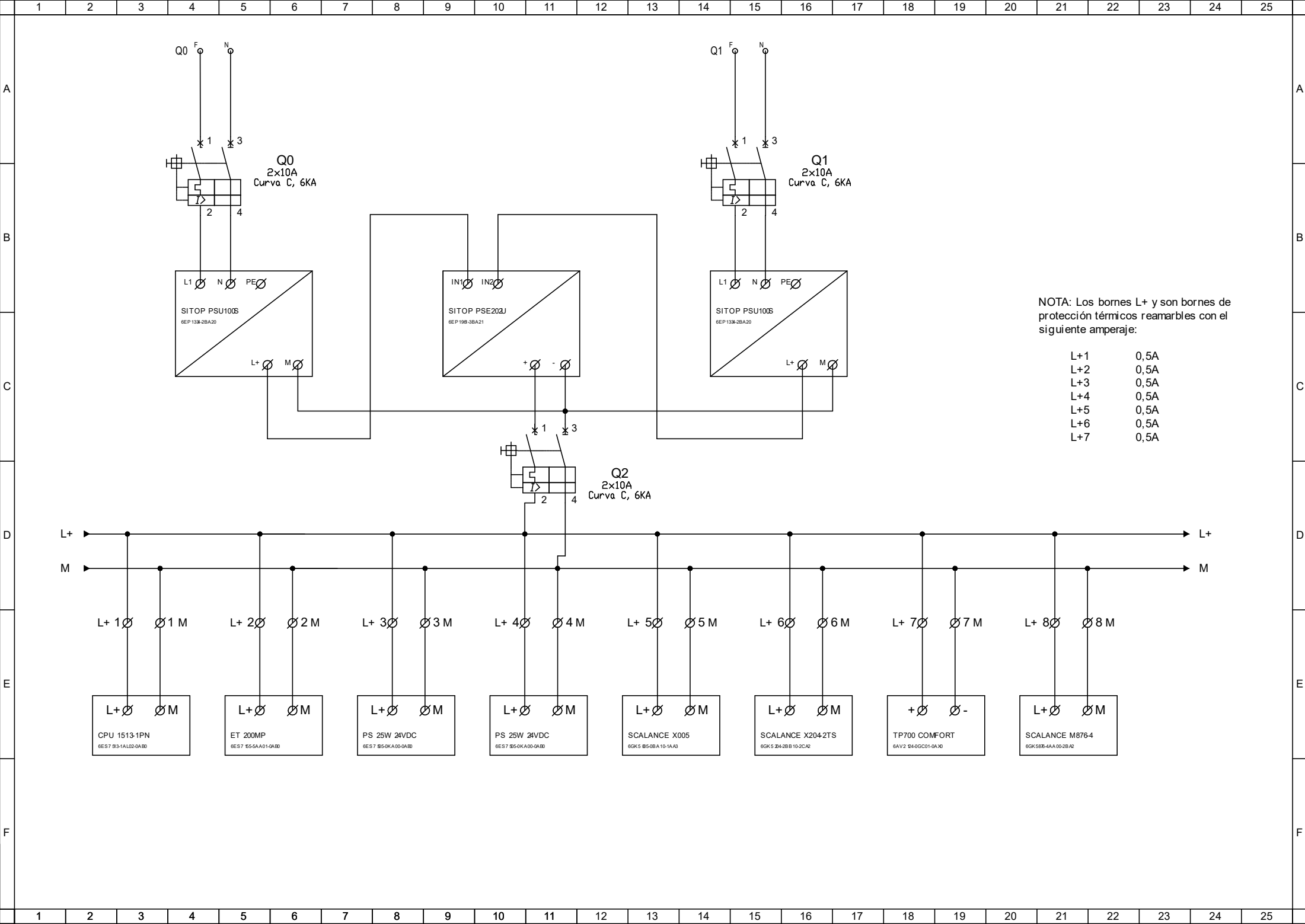
INDICE

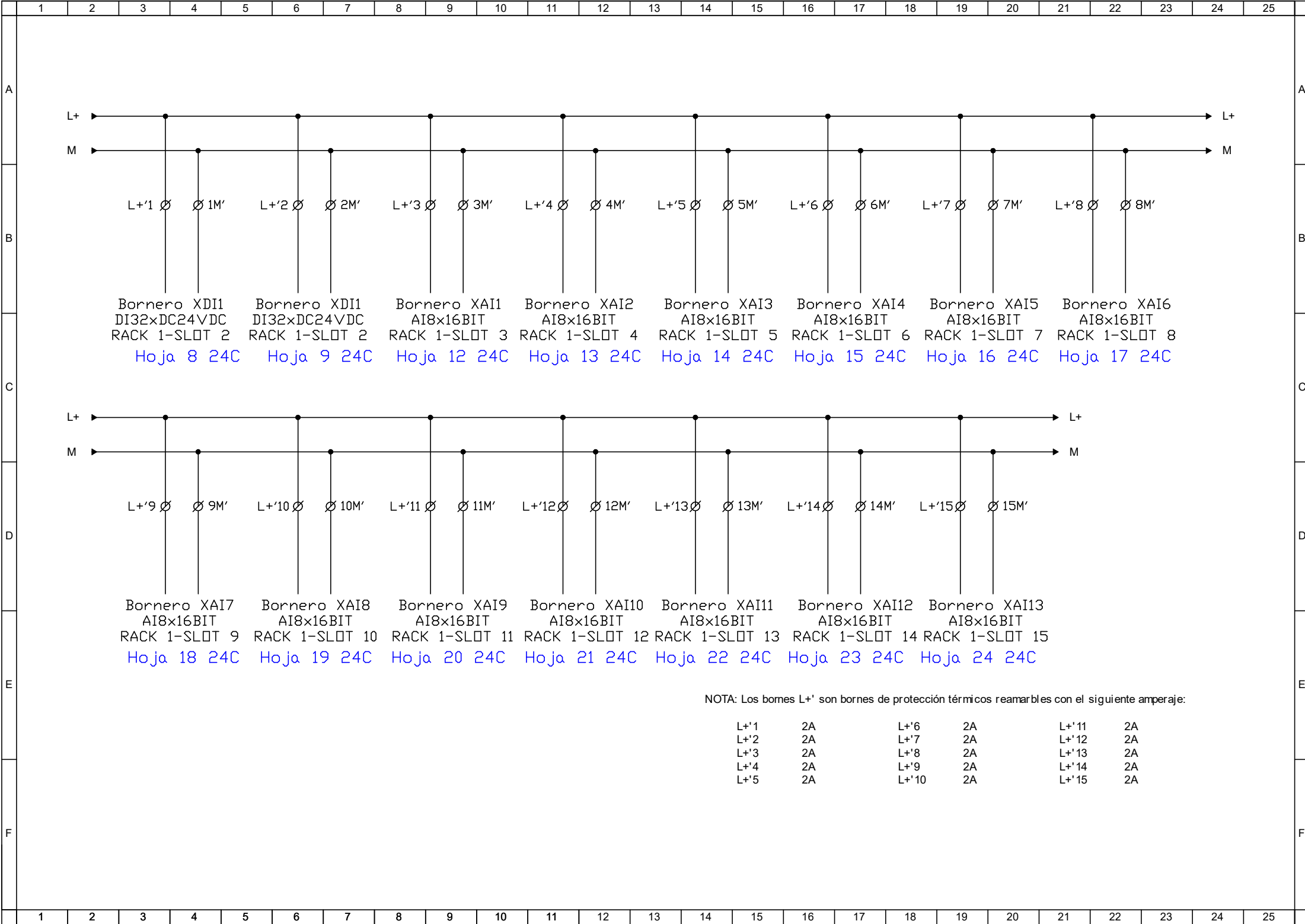
SL.NO.	DESCRIPCION	REV. NO.	REV DESCRIPC	FECHA
25	AQ8x16BIT - RACK 2 - SLOT 3 - BORNERO XA01	1	AS BUILT	AGO. 20
26	AQ8x16BIT - RACK 2 - SLOT 4 - BORNERO XA02	1	AS BUILT	AGO. 20
27	AQ8x16BIT - RACK 2 - SLOT 5 - BORNERO XA03	1	AS BUILT	AGO. 20
28	AQ8x16BIT - RACK 2 - SLOT 6 - BORNERO XA04	1	AS BUILT	AGO. 20
29	AQ8x16BIT - RACK 2 - SLOT 7 - BORNERO XA05	1	AS BUILT	AGO. 20
30	AQ8x16BIT - RACK 2 - SLOT 8 - BORNERO XA06	1	AS BUILT	AGO. 20
31	AQ8x16BIT - RACK 2 - SLOT 9 - BORNERO XA07	1	AS BUILT	AGO. 20
32	AQ8x16BIT - RACK 2 - SLOT 10 - BORNERO XA08	1	AS BUILT	AGO. 20
33	AQ8x16BIT - RACK 2 - SLOT 11 - BORNERO XA09	1	AS BUILT	AGO. 20
34	AQ8x16BIT - RACK 2 - SLOT 12 - BORNERO XA010	1	AS BUILT	AGO. 20
35	AQ8x16BIT - RACK 2 - SLOT 13 - BORNERO XA011	1	AS BUILT	AGO. 20
36	AQ8x16BIT - RACK 2 - SLOT 14 - BORNERO XA012	1	AS BUILT	AGO. 20
37	AQ8x16BIT - RACK 2 - SLOT 15 - BORNERO XA013	1	AS BUILT	AGO. 20
38	CONEXIONADO ETHERNET	1	AS BUILT	AGO. 20

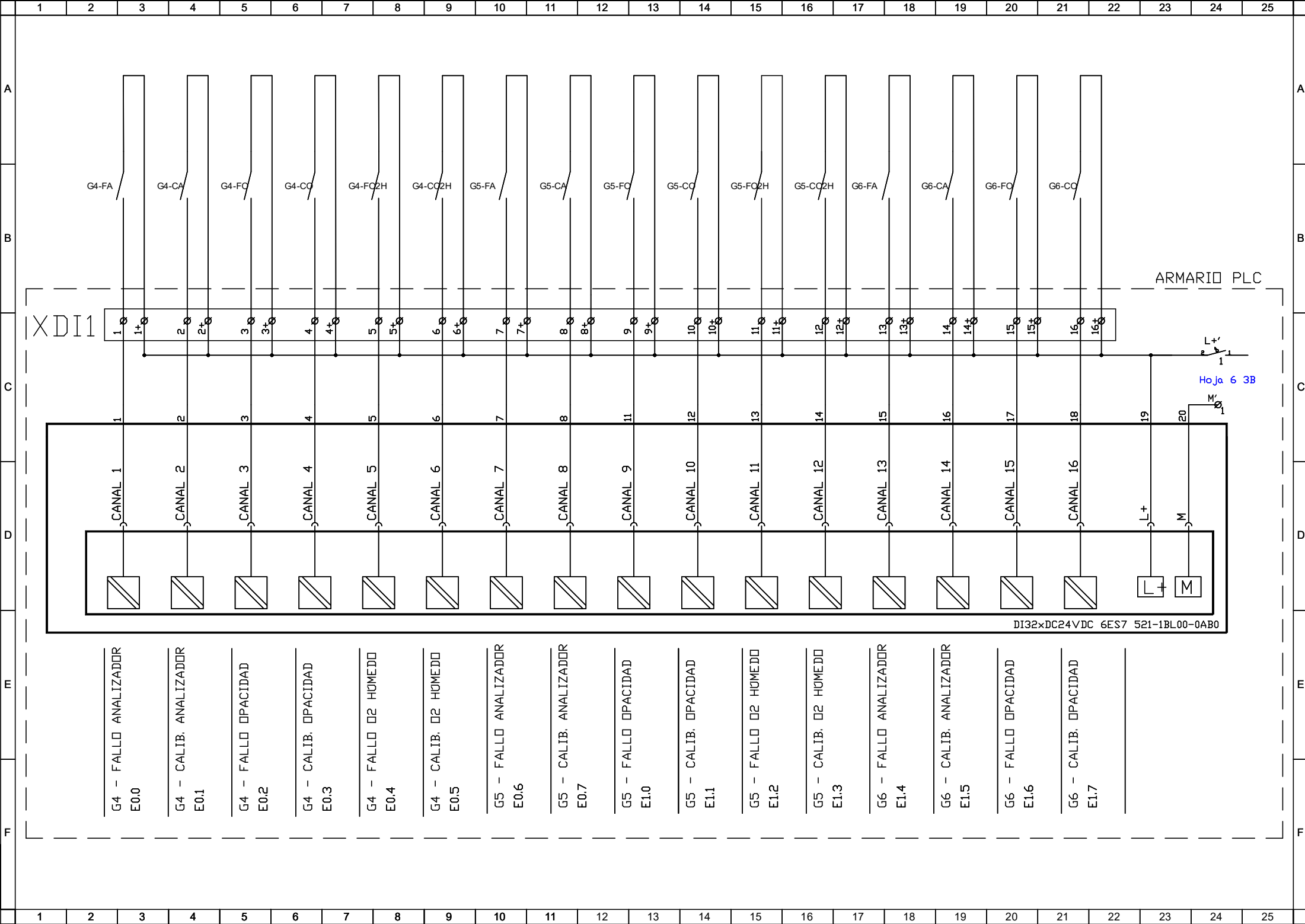
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	
A																										A
B																										B
C																										C
D																										D
E																										E
F																										F
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	

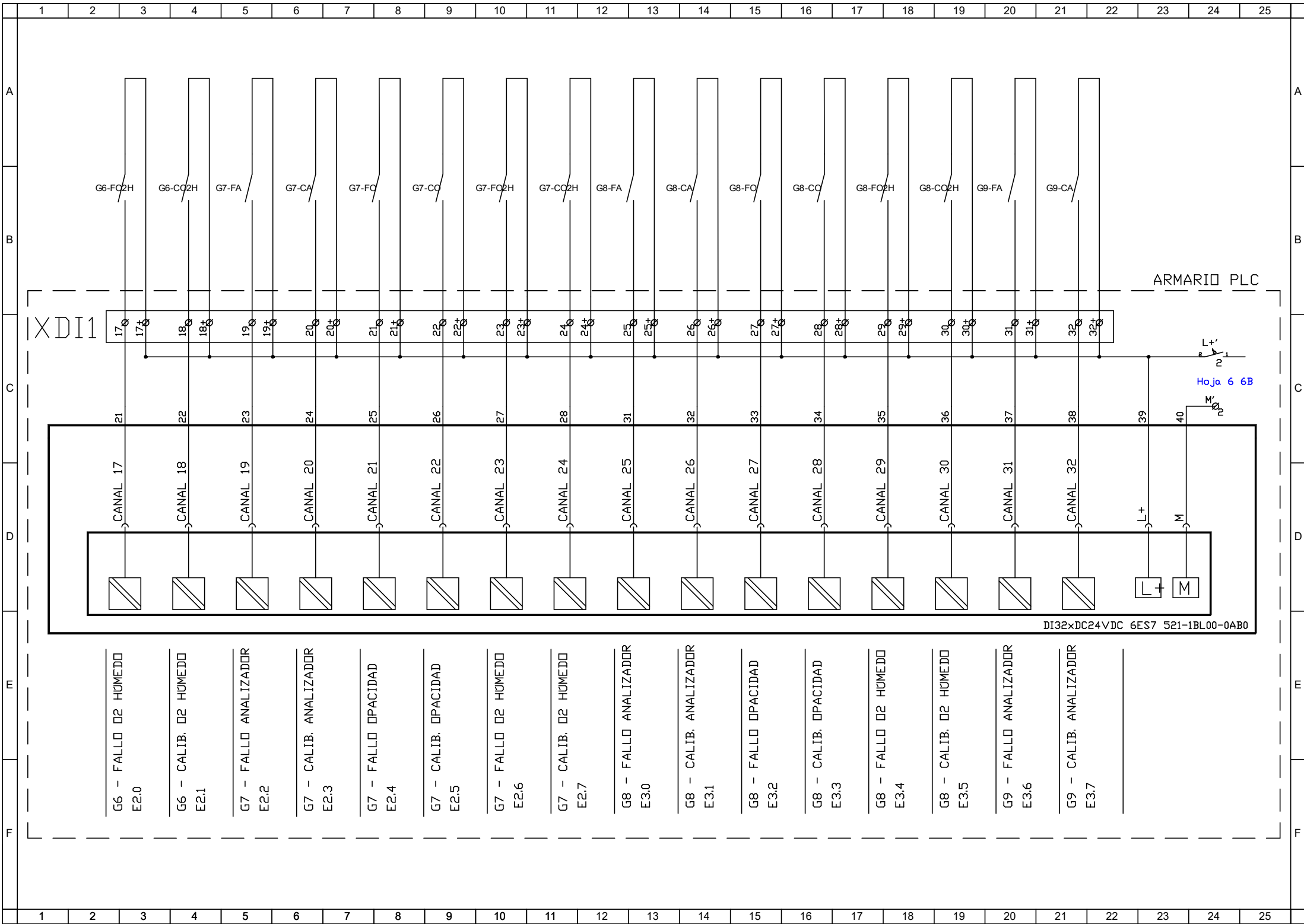
NOTA: El bornero X0 se reserva para Tensión Segura y el X1 para Tensión Normal.

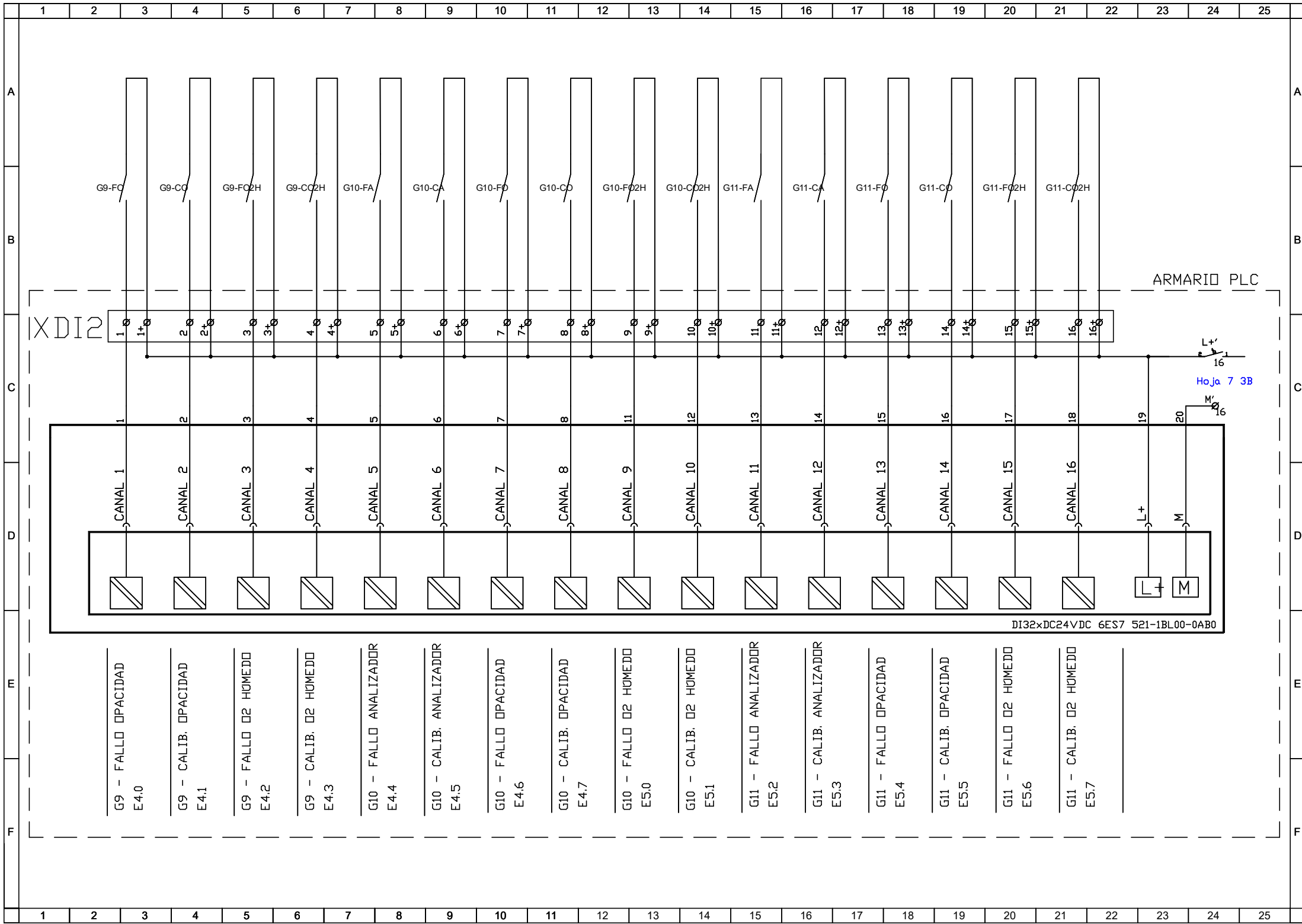
NOTA: El bornero X0 se reserva para Tensión Segura y el X1 para Tensión Normal.

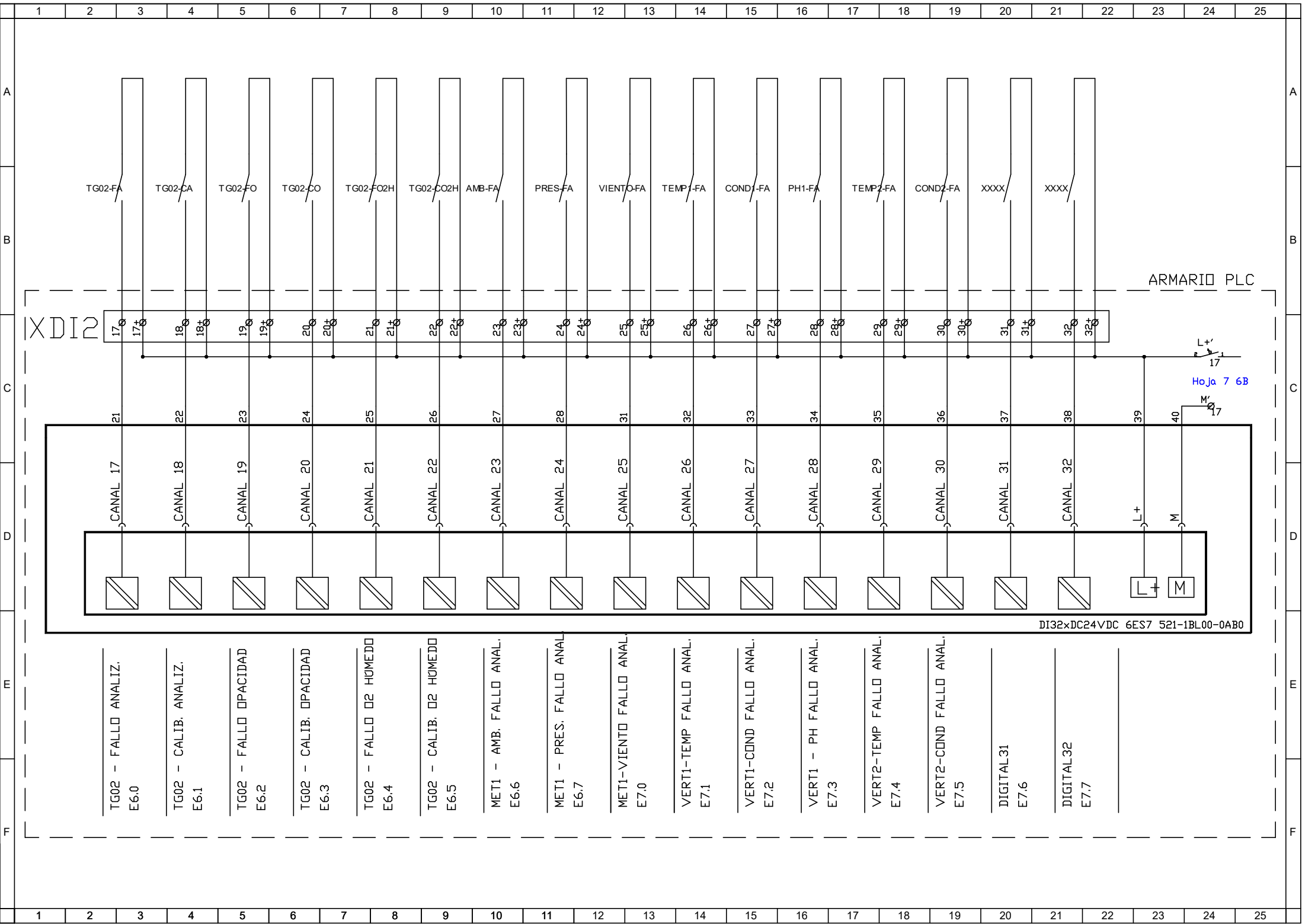


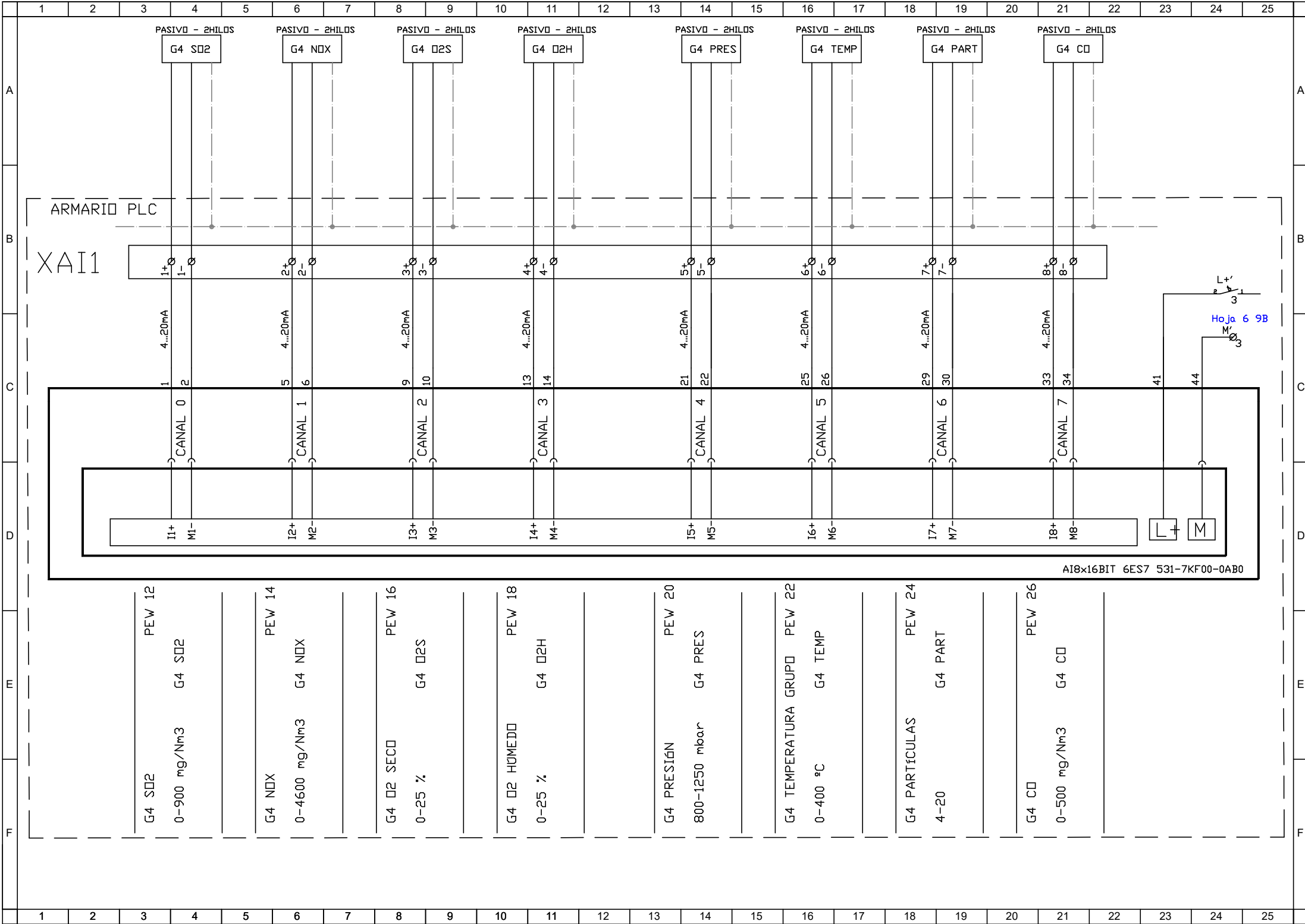


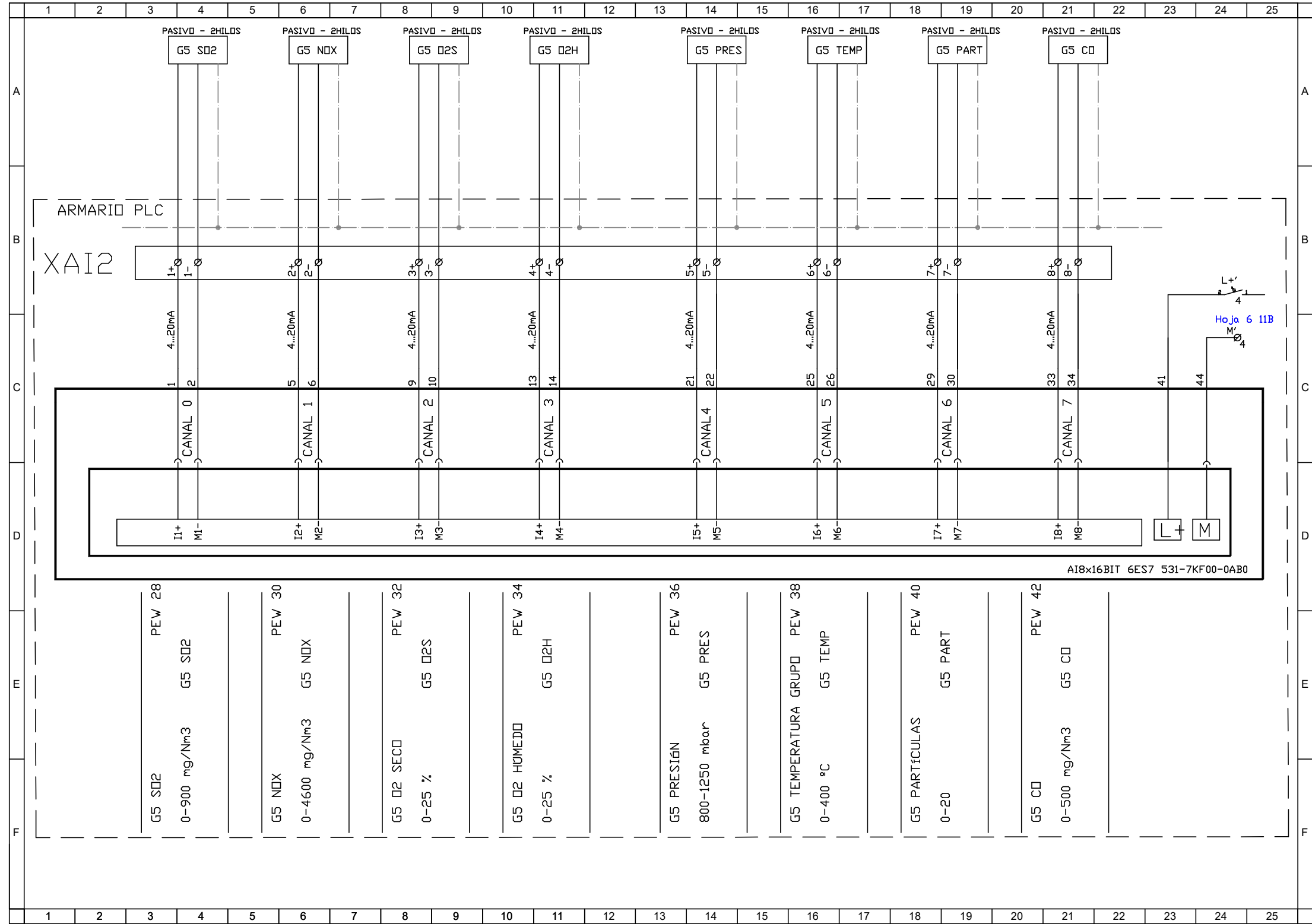


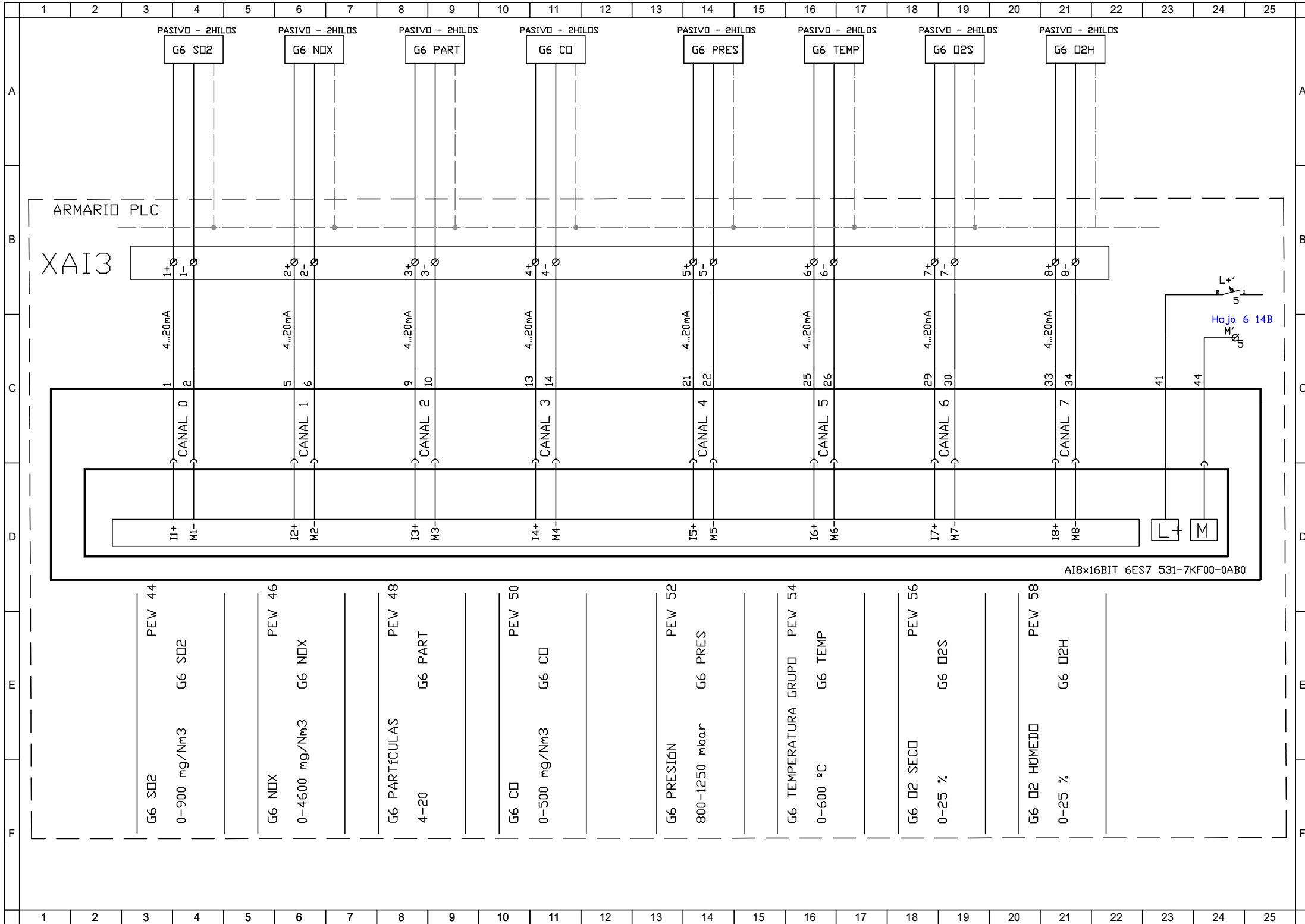


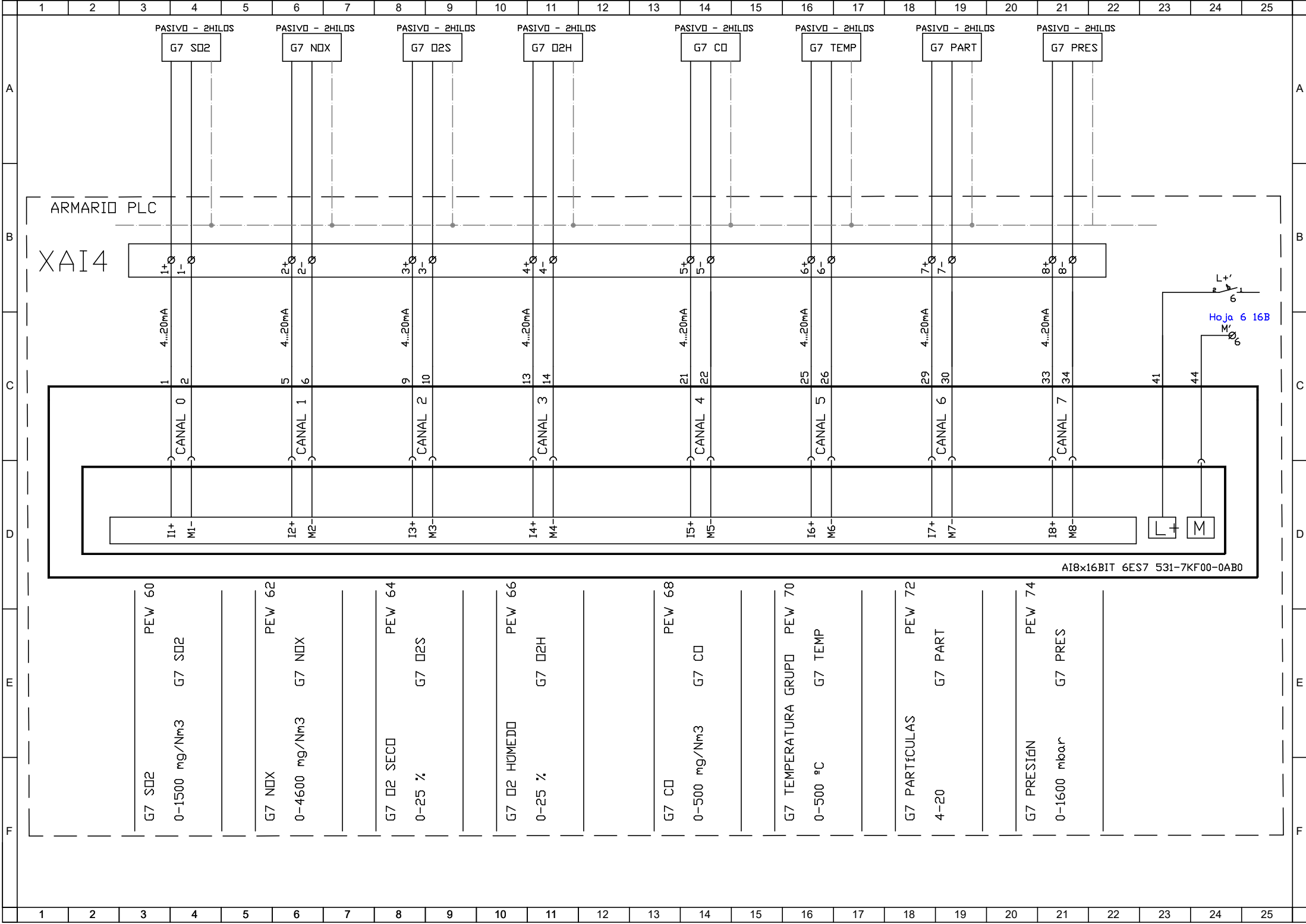


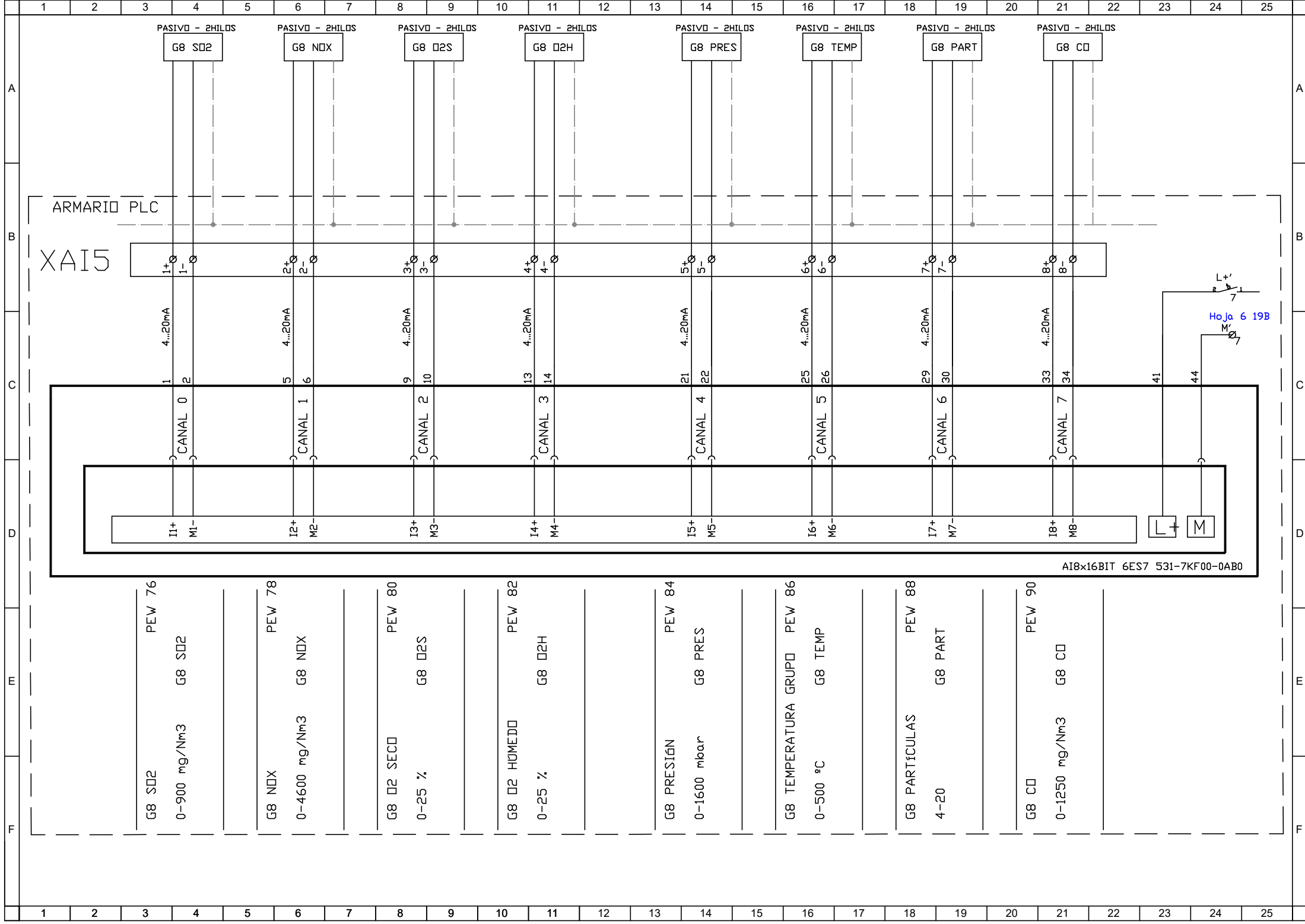


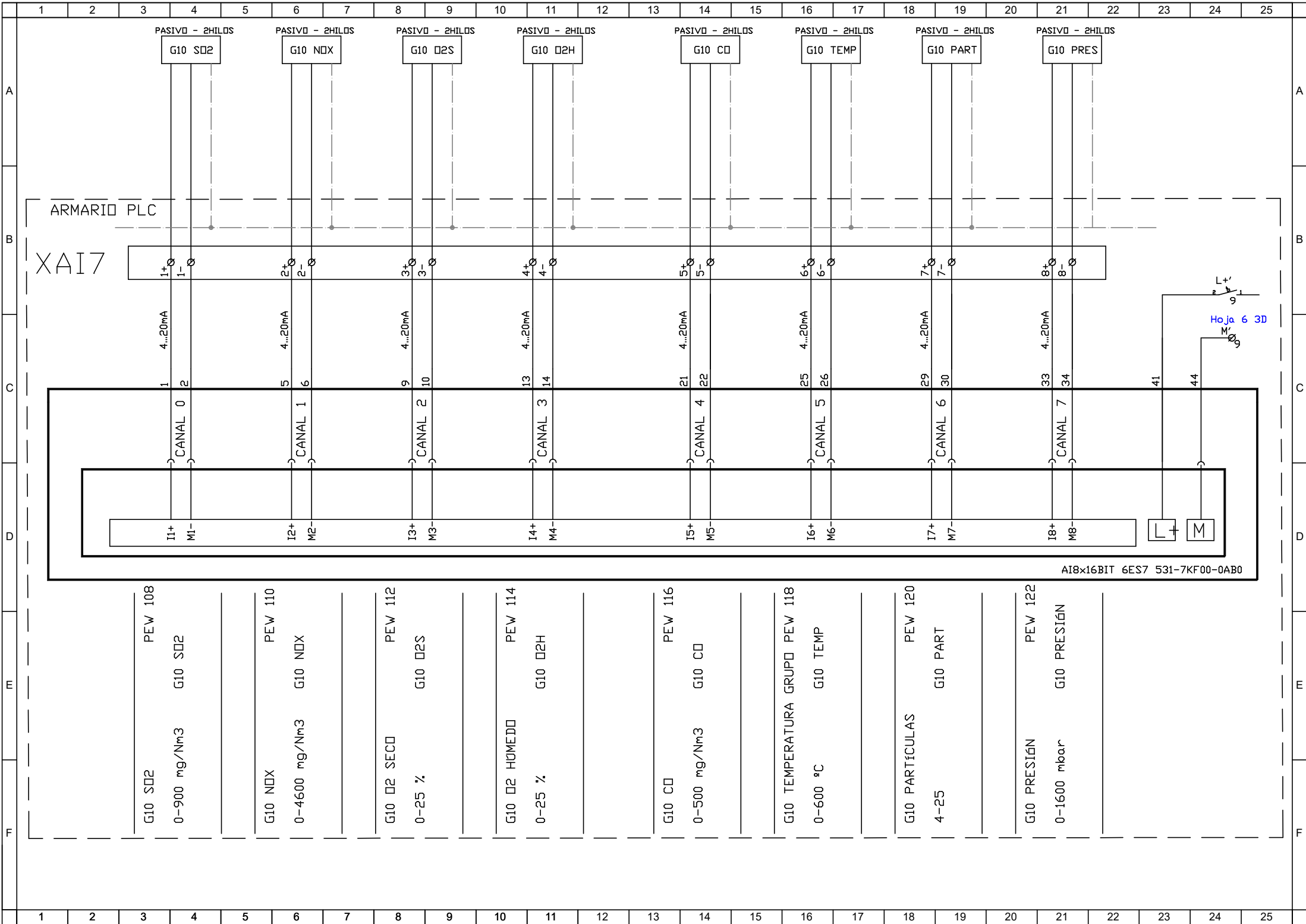


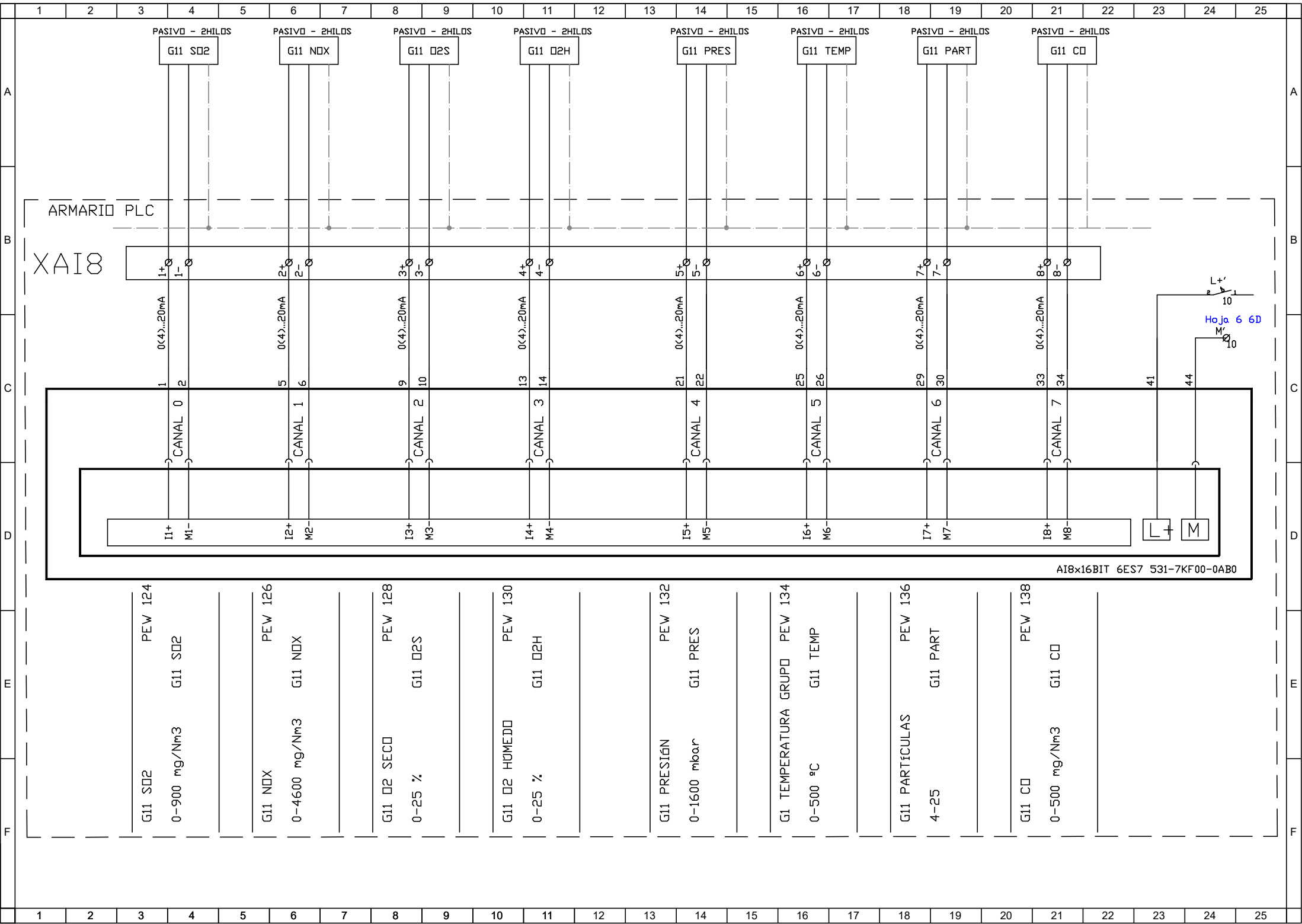


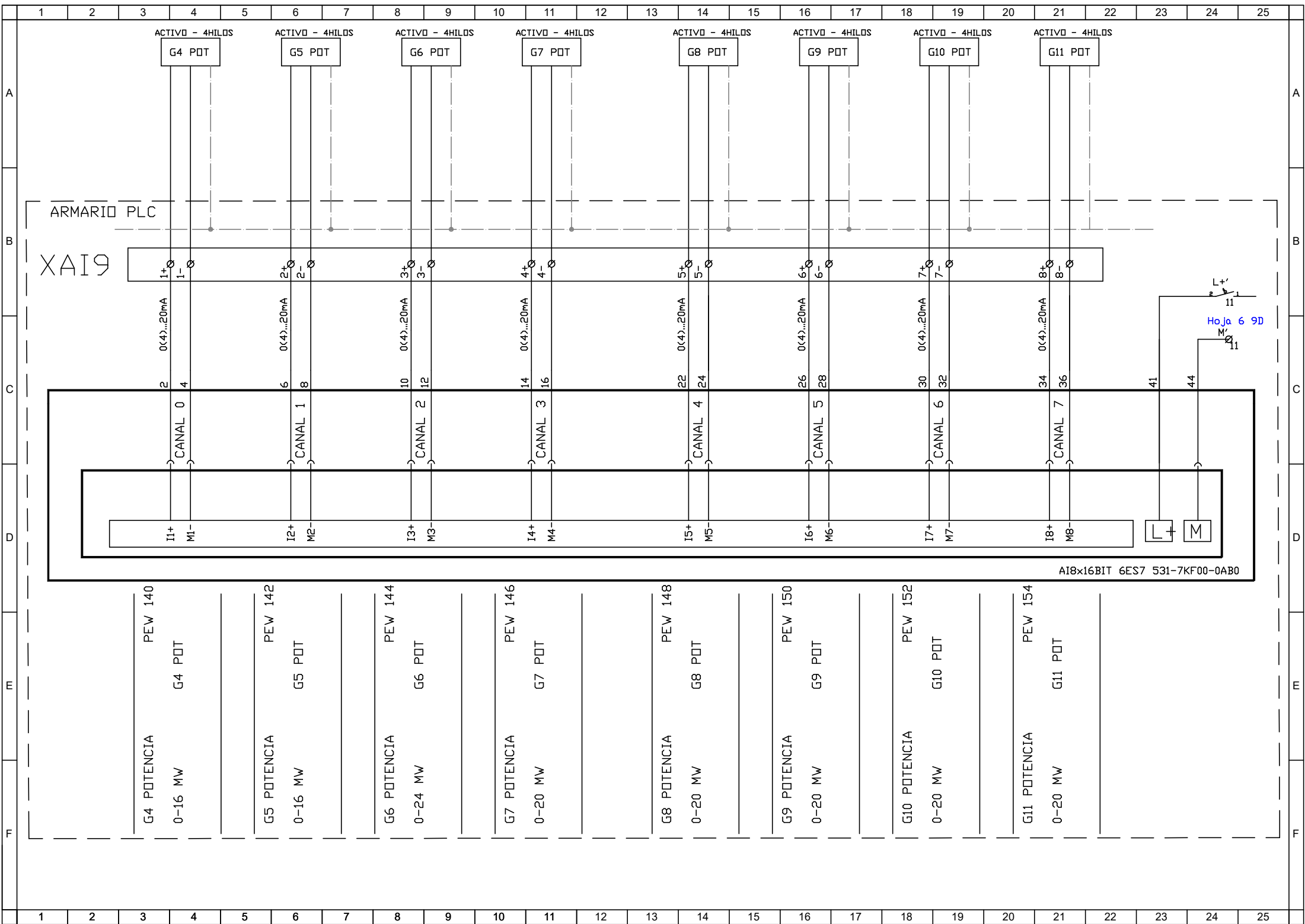












5+

5-

22

24

CANAL 4

I5+

M5-

0(4)...20mA

6+

6-

26

28

CANAL 5

I6+

M6-

0(4)...20mA

7+

7-

30

32

CANAL 6

I7+

M7-

0(4)...20mA

8+

8-

34

36

CANAL 7

I8+

M8-

0(4)...20mA

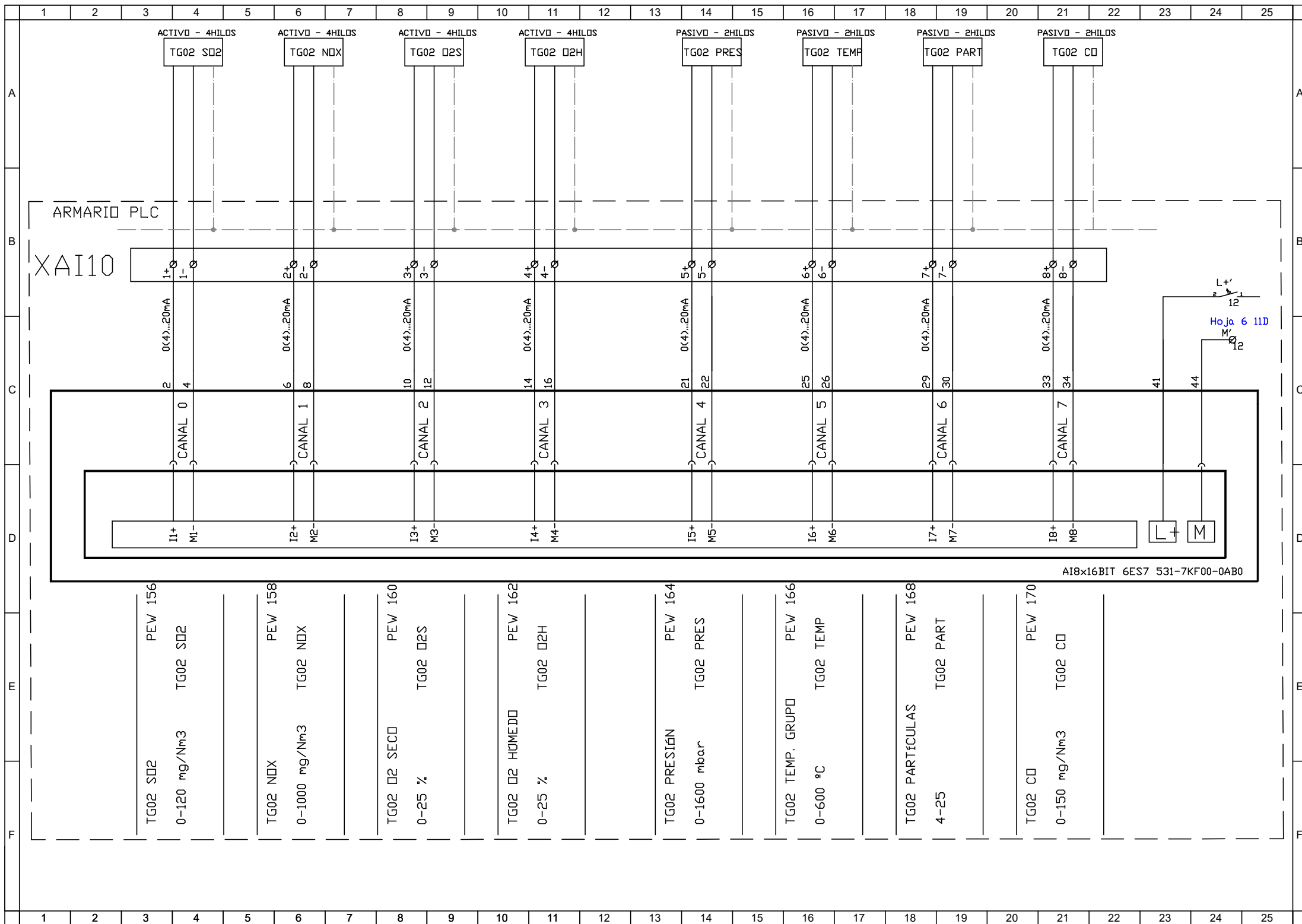
41

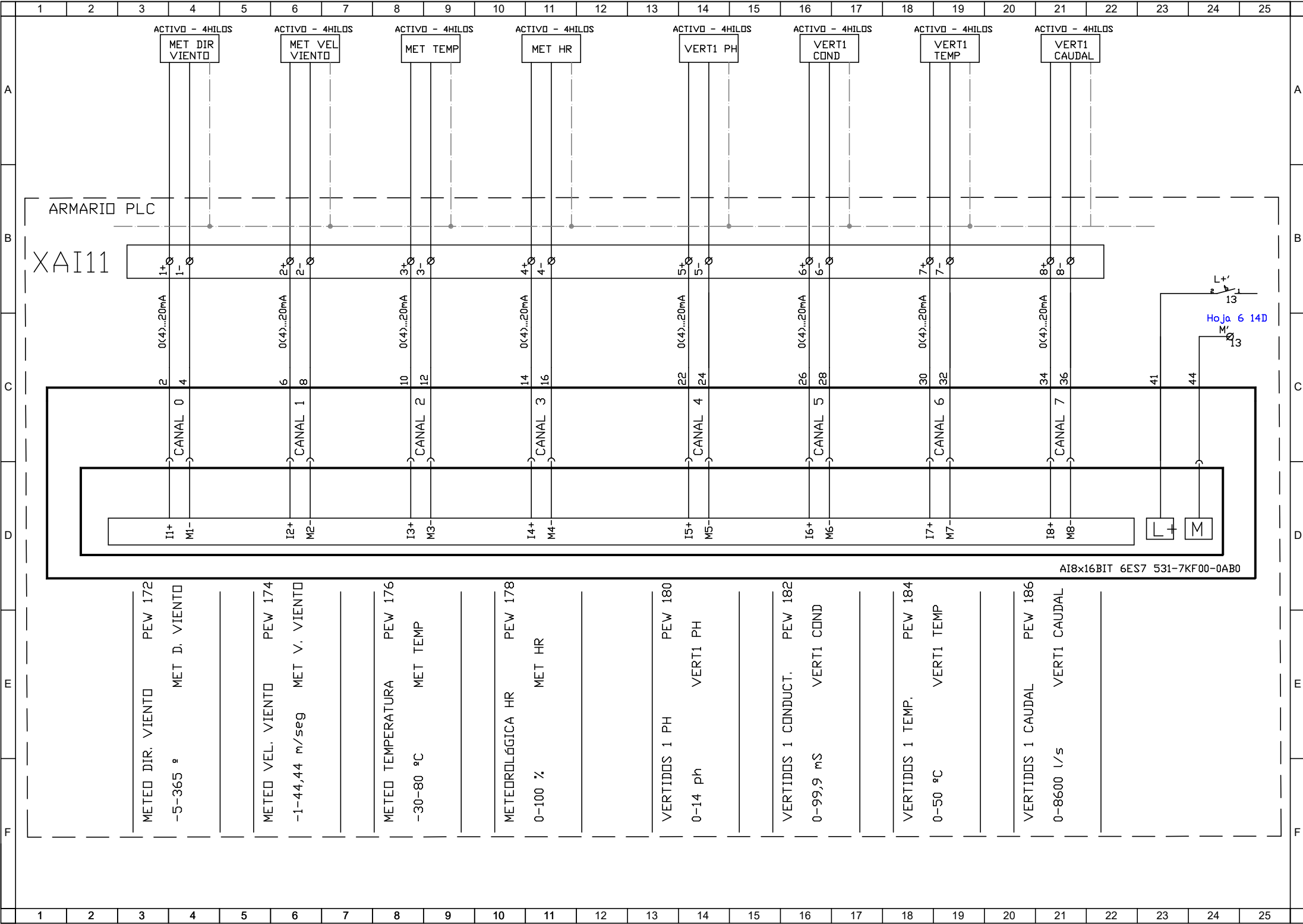
44

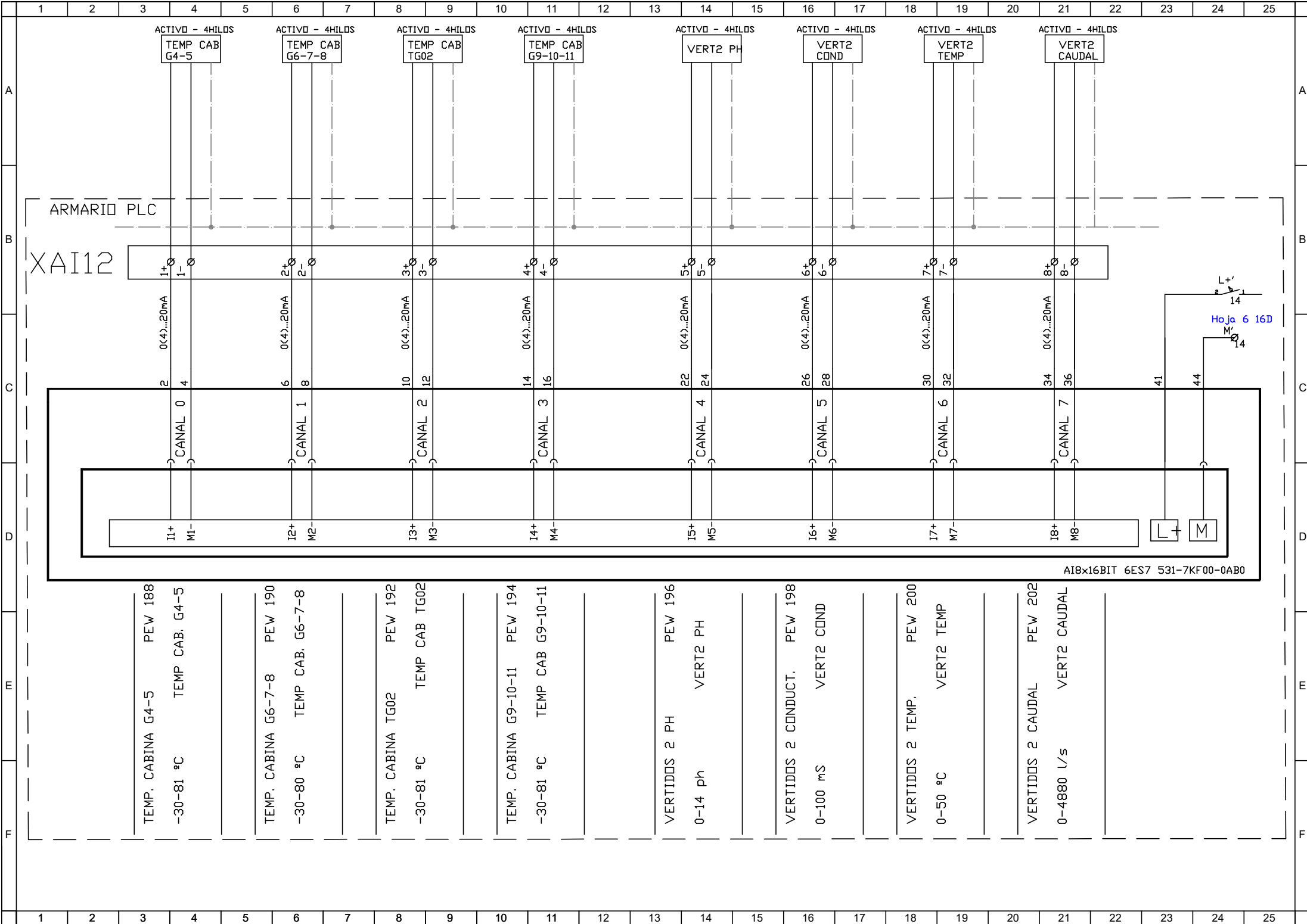
L+

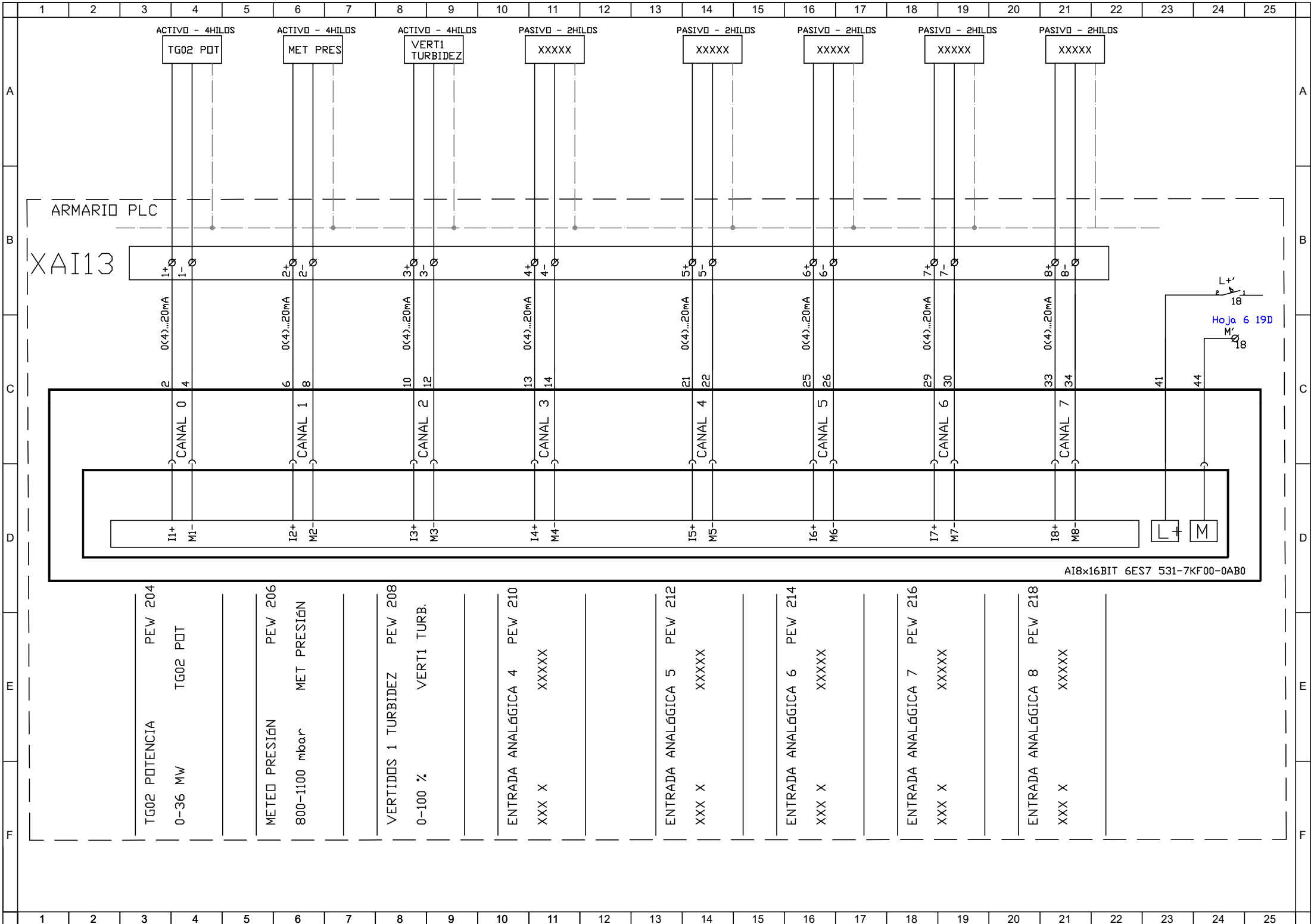
M

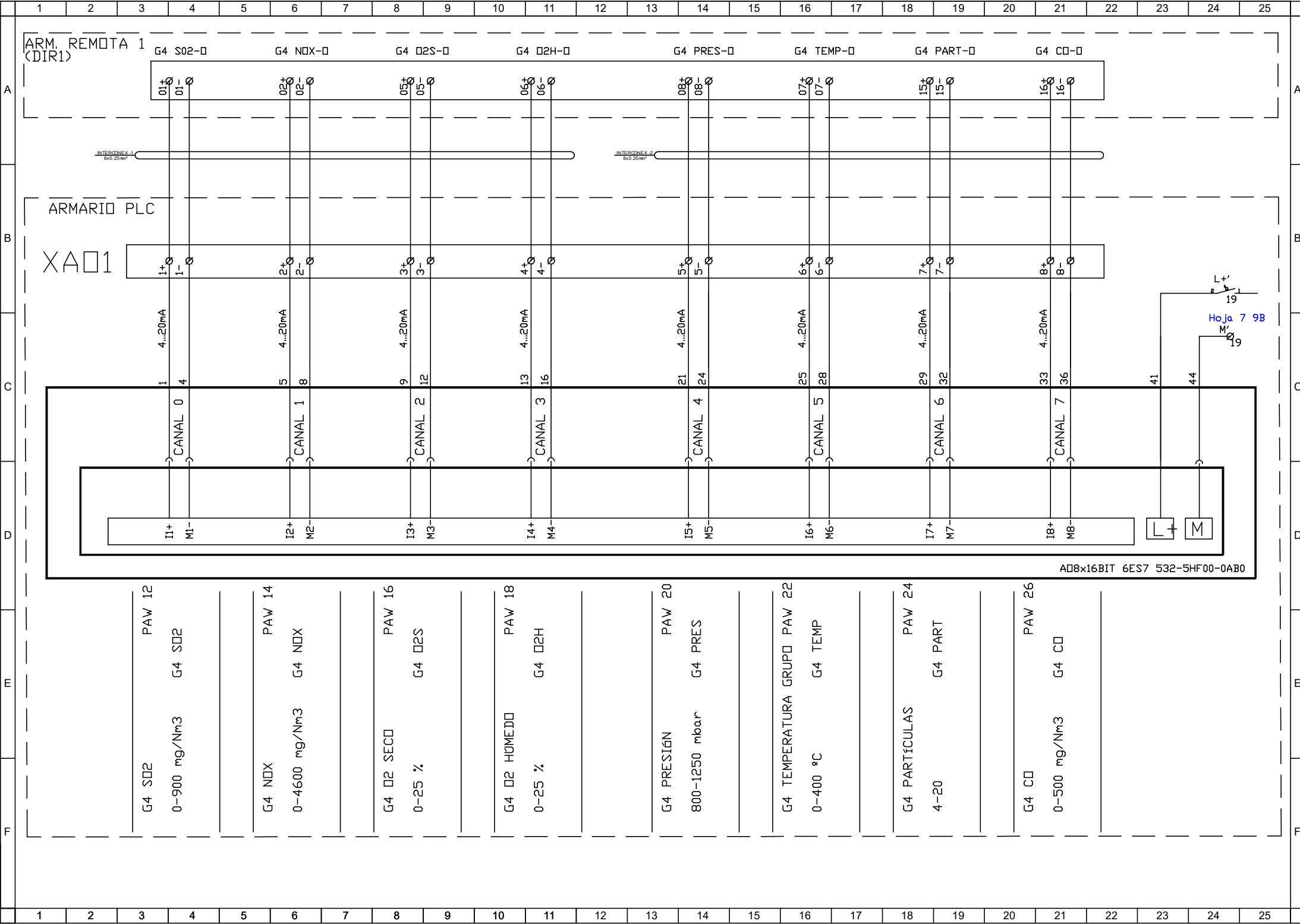
Hoja 6 9D











07+

07-

Ø

15+

15-

Ø

16+

16-

Ø

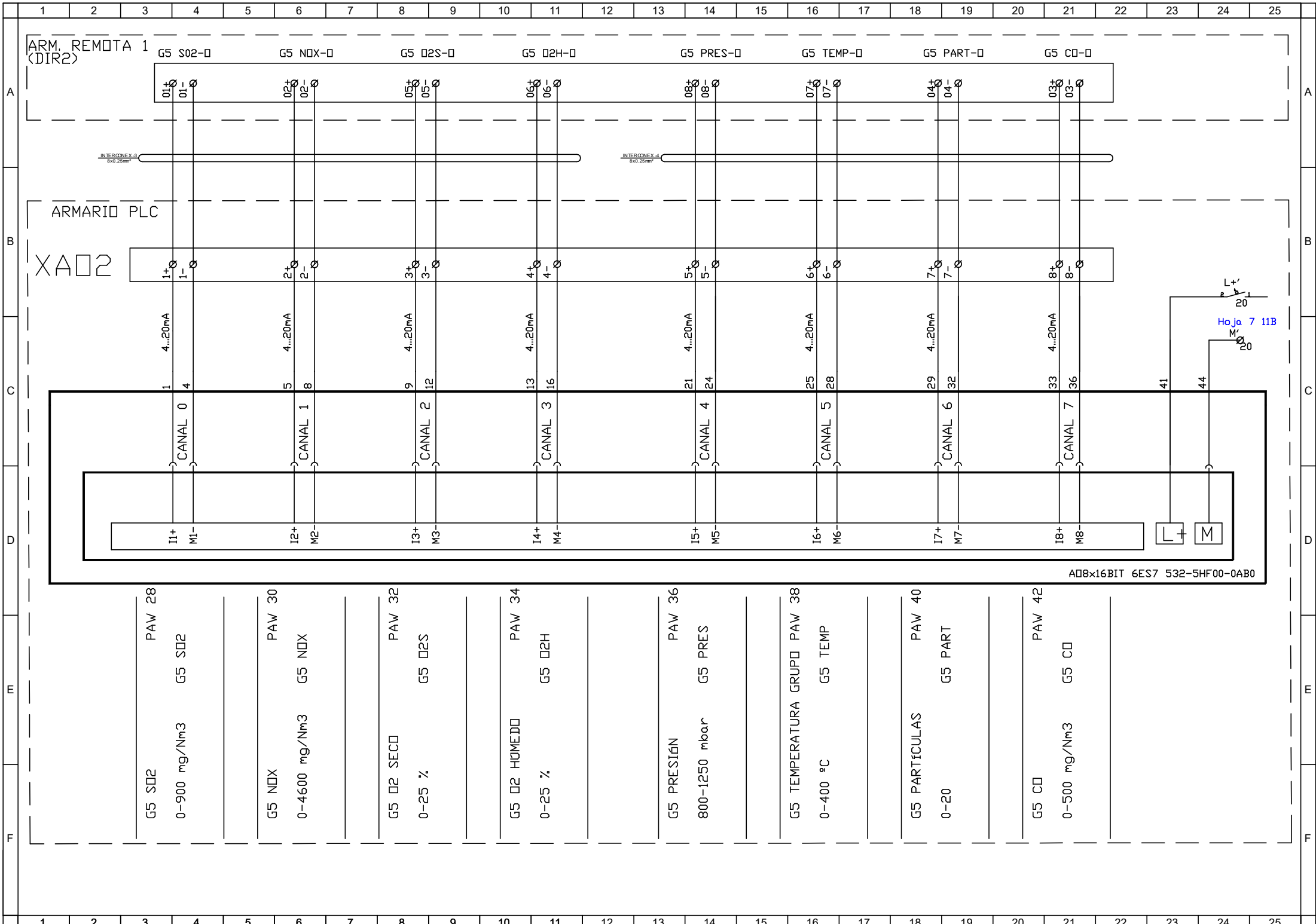
INTERCONEX-1

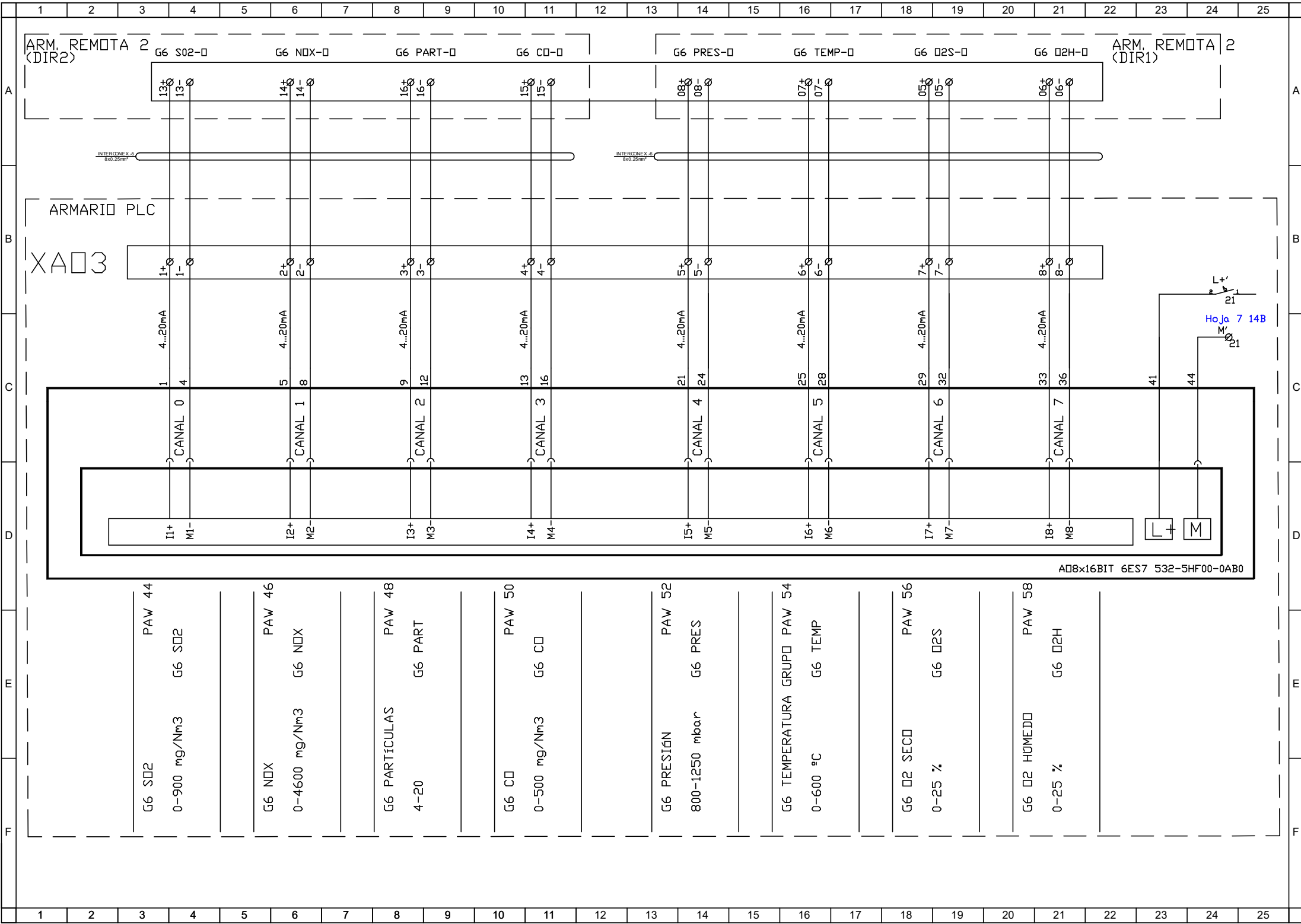
8x0.25mm²

INTERCONEX-2

8x0.25mm²

L+
19
HoJa 7 9B
M'
19



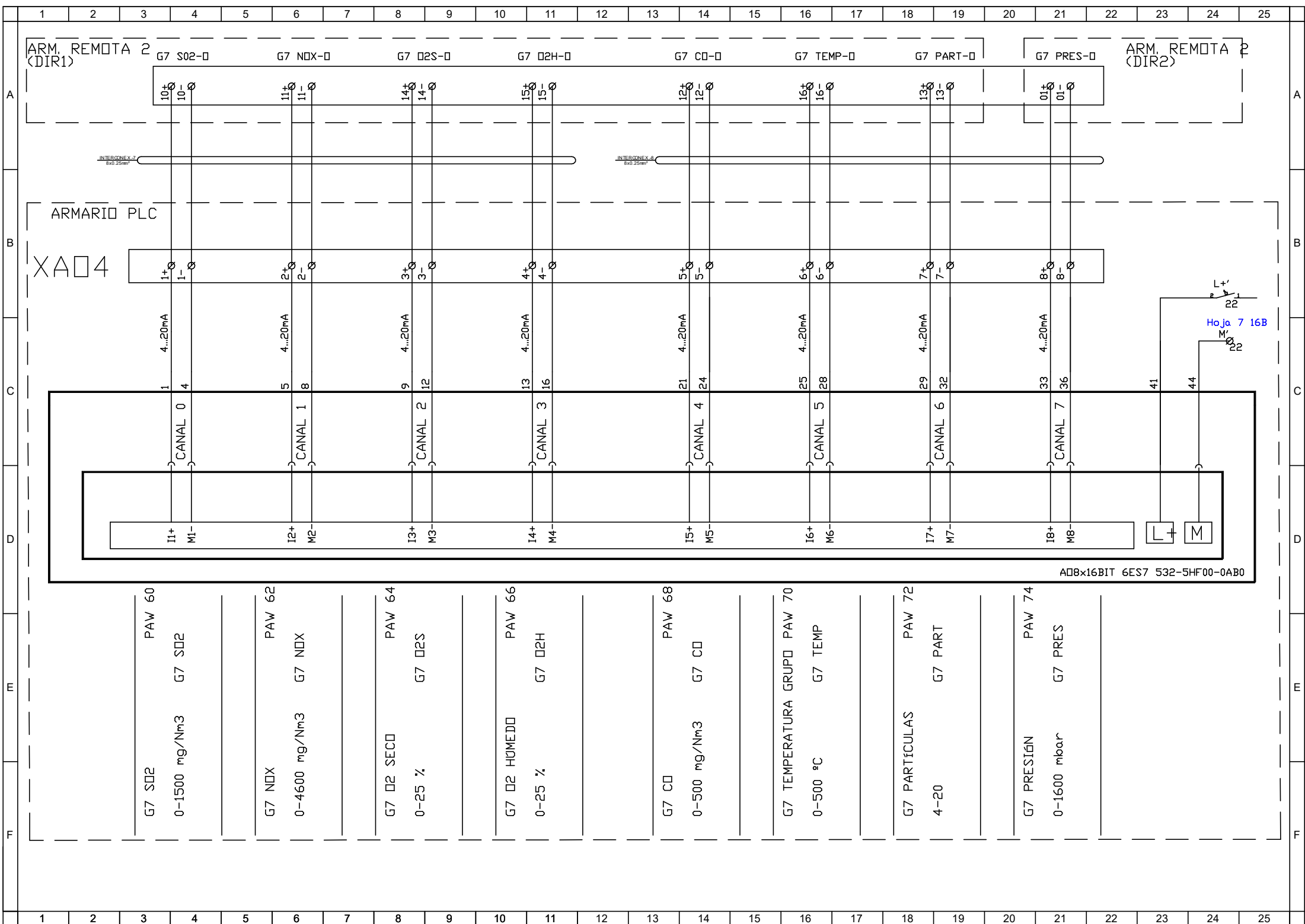


L+

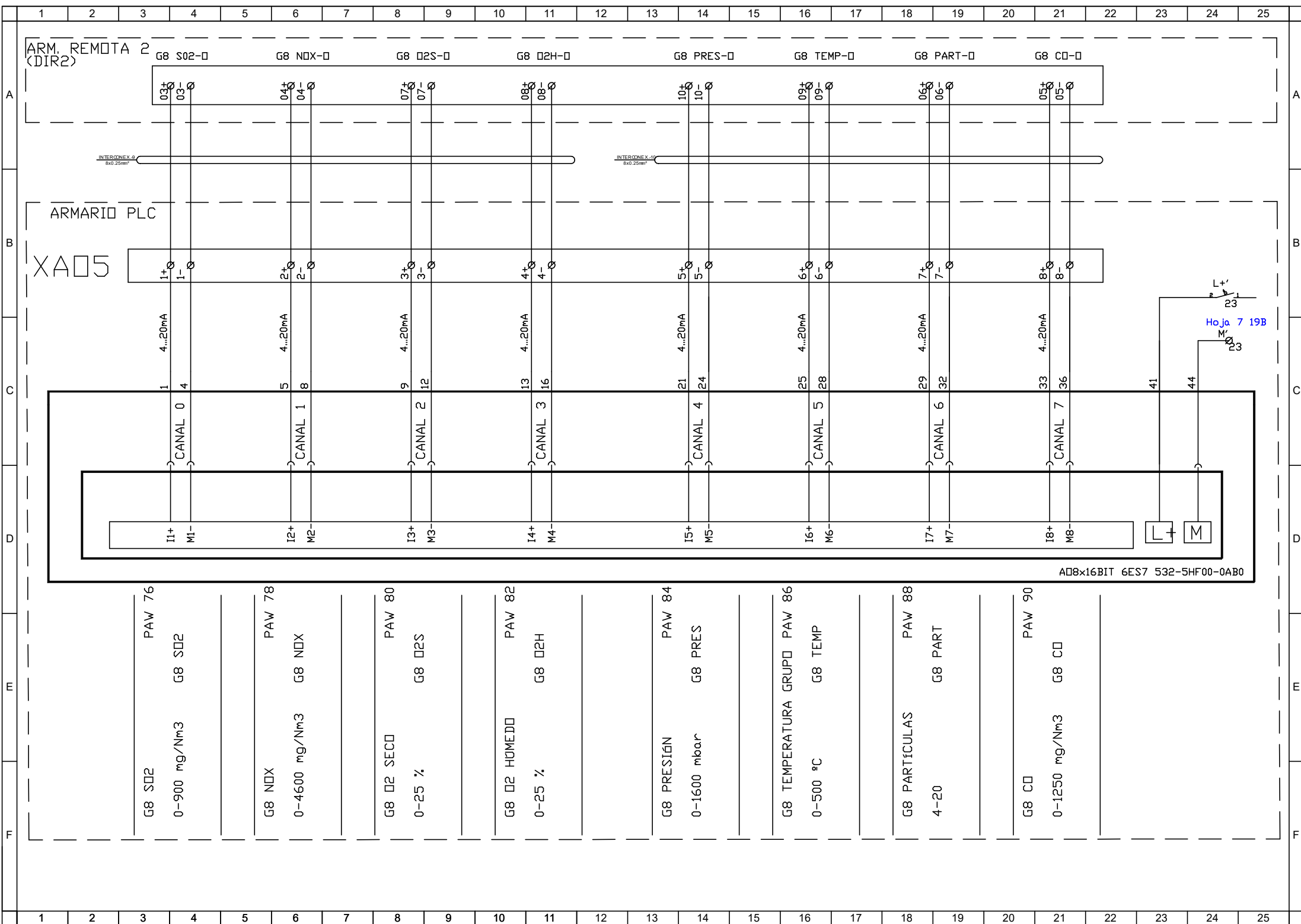
M

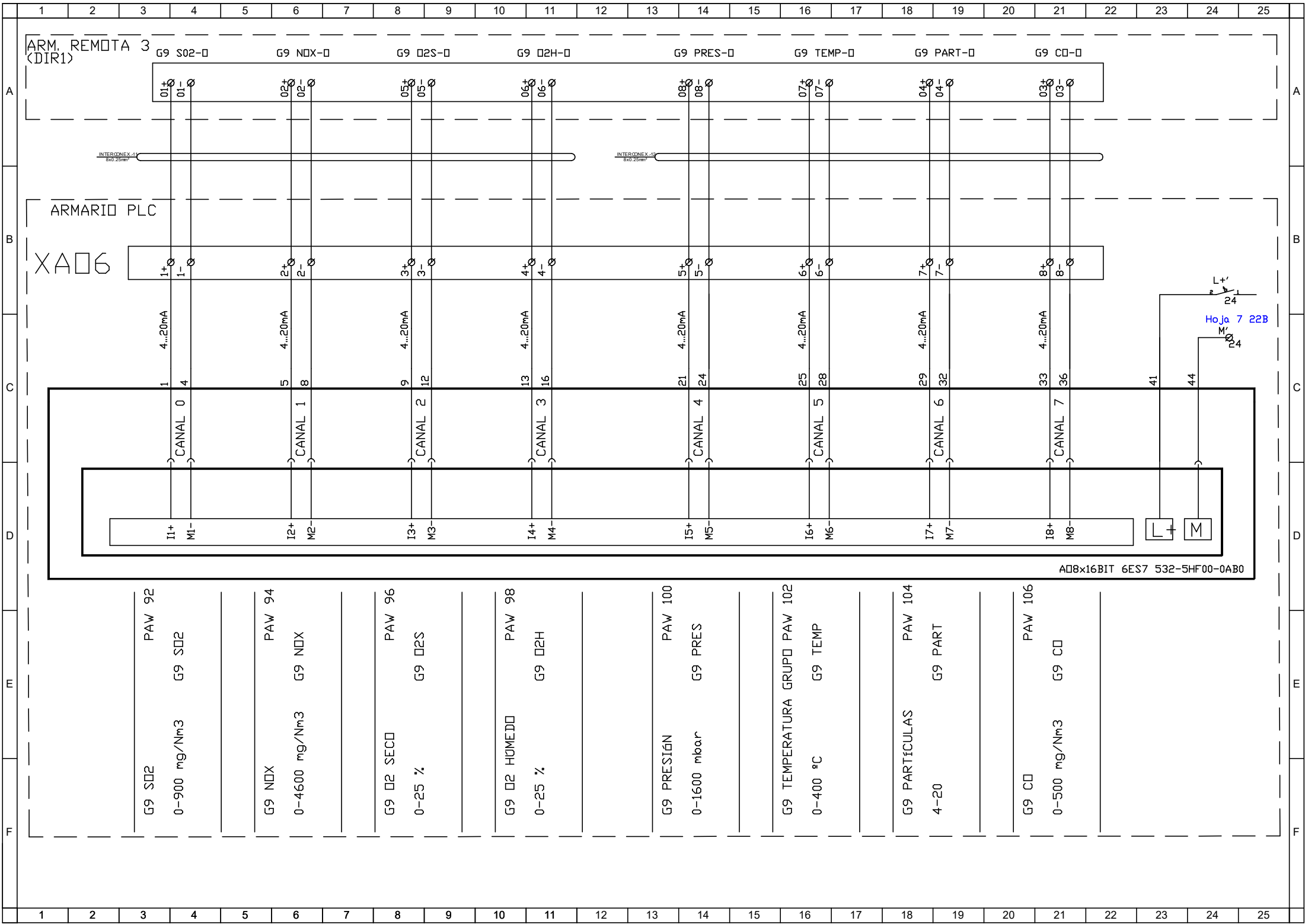
L+

M



L+
22
HoJa 7 16B
M+
22





07+

07-

Ø

04+

04-

Ø

03+

03-

Ø

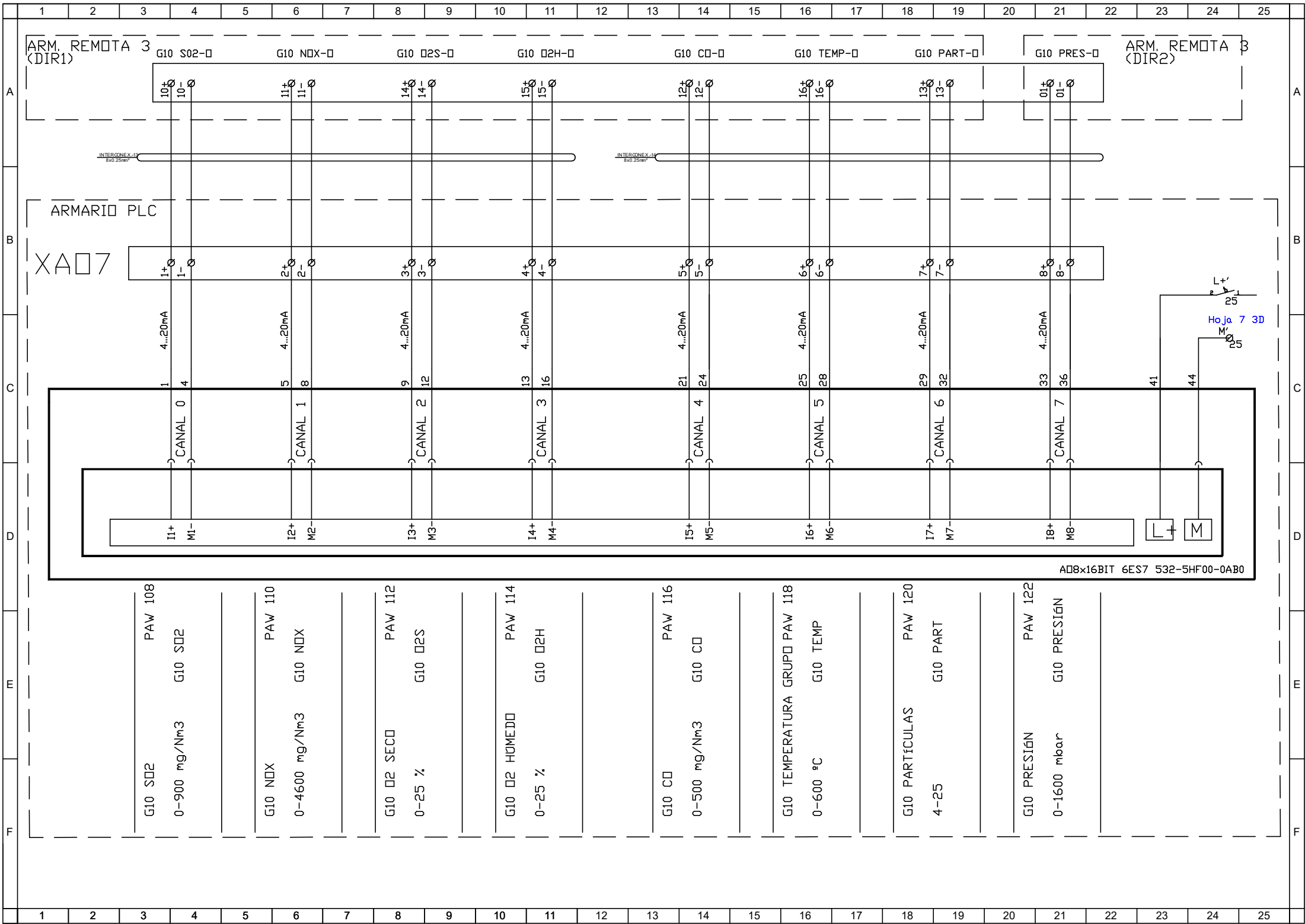
INTERCONEX-14

6x0.25mm²

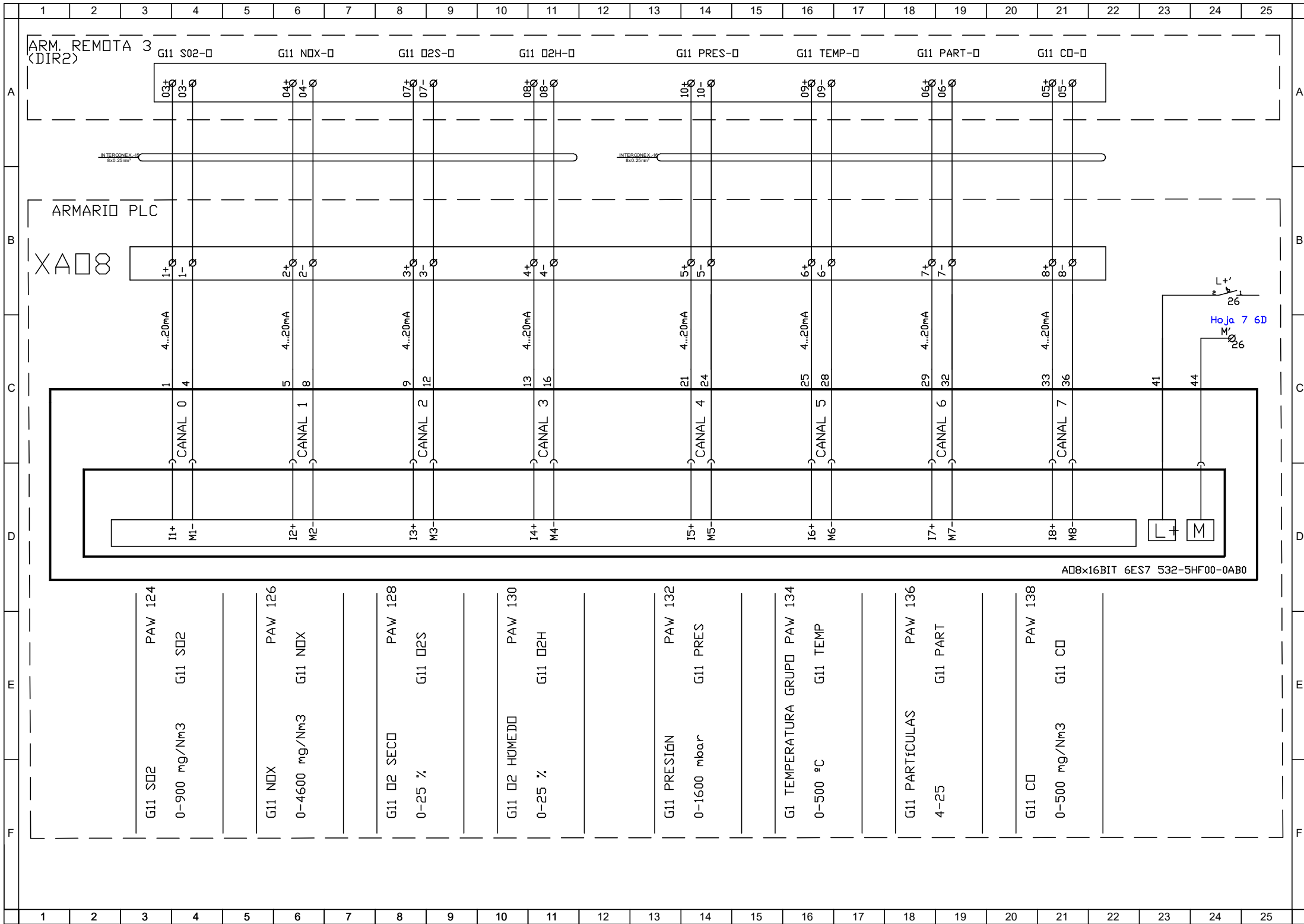
INTERCONEX-15

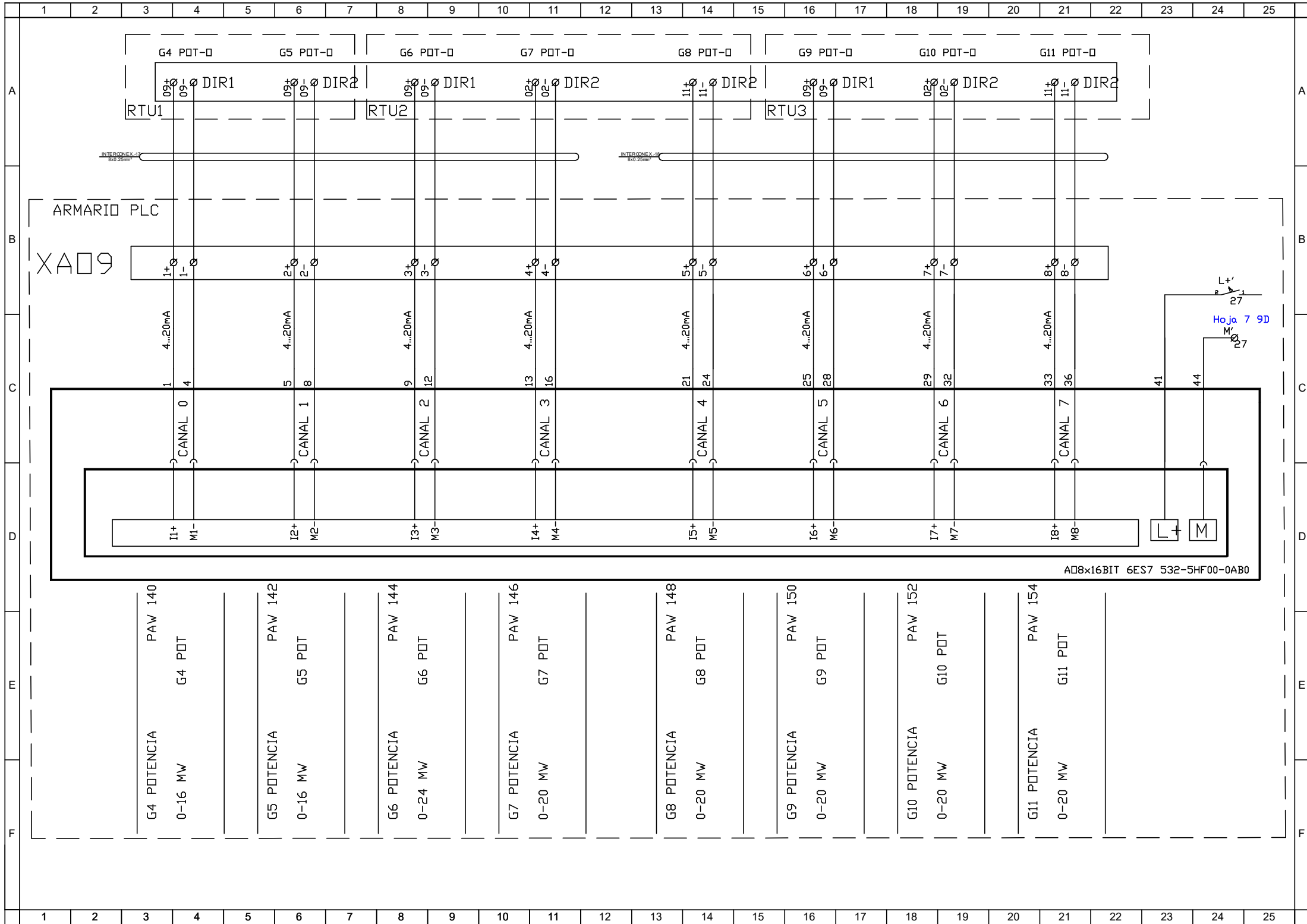
6x0.25mm²

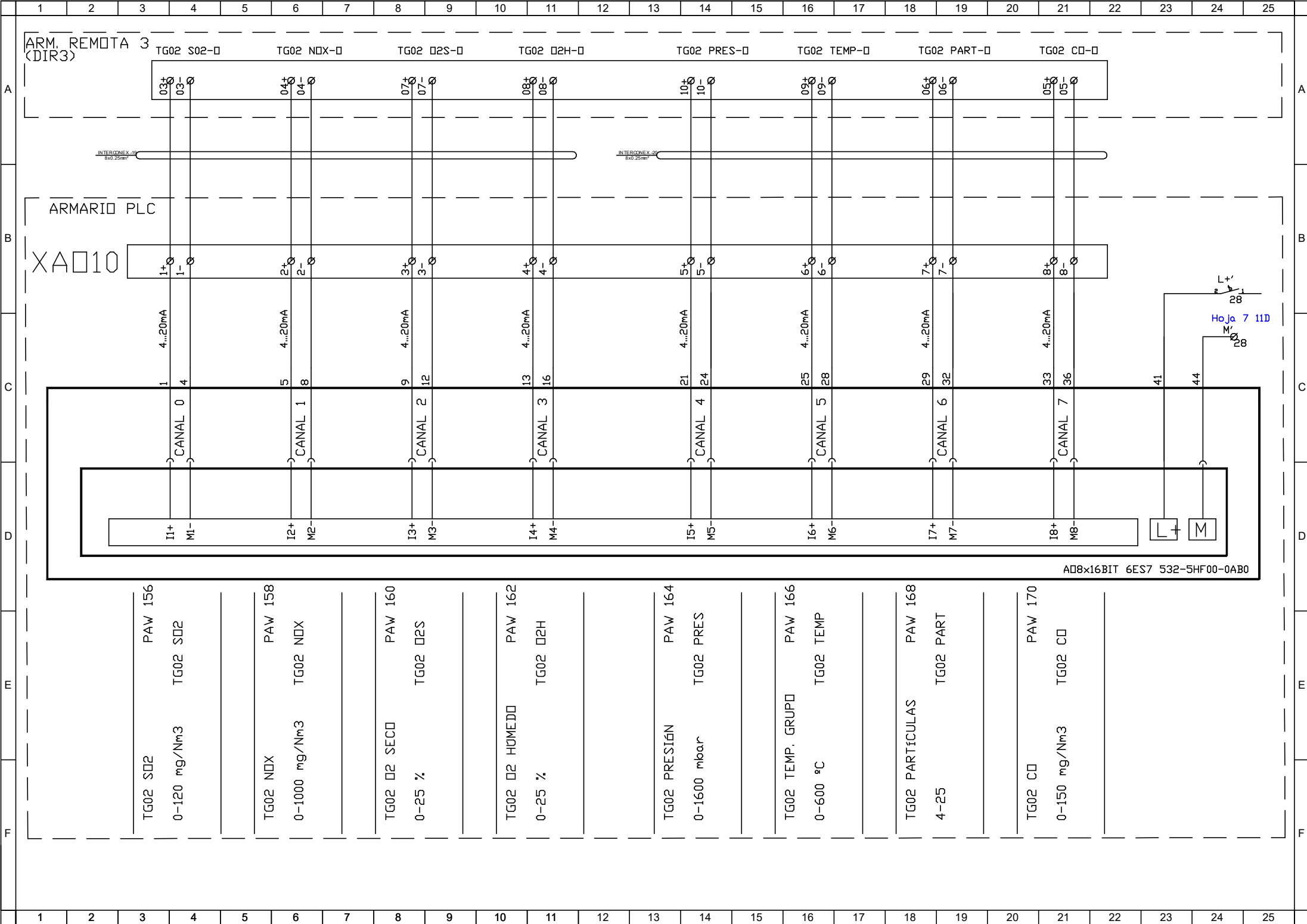
Hoja 7 22B

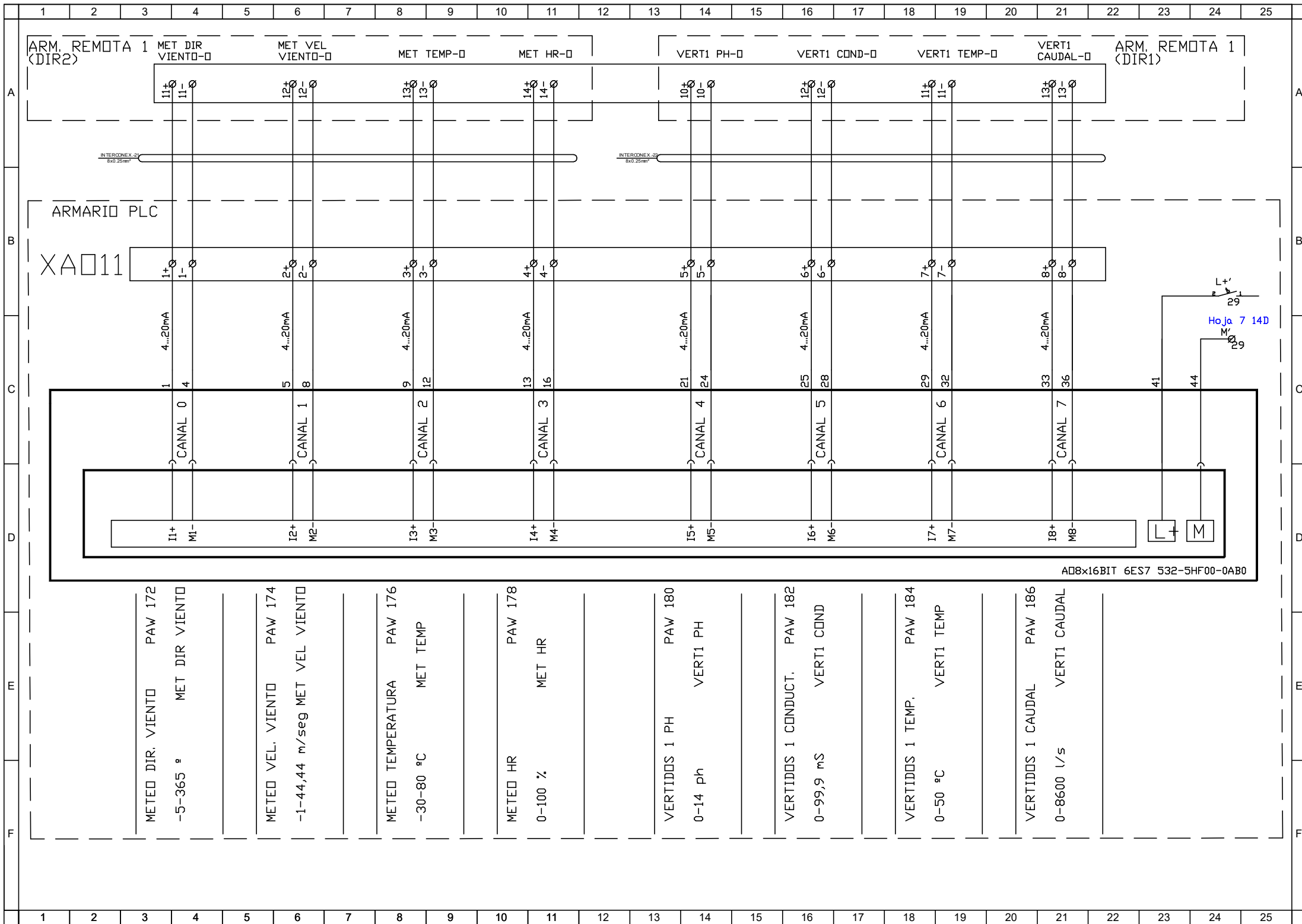


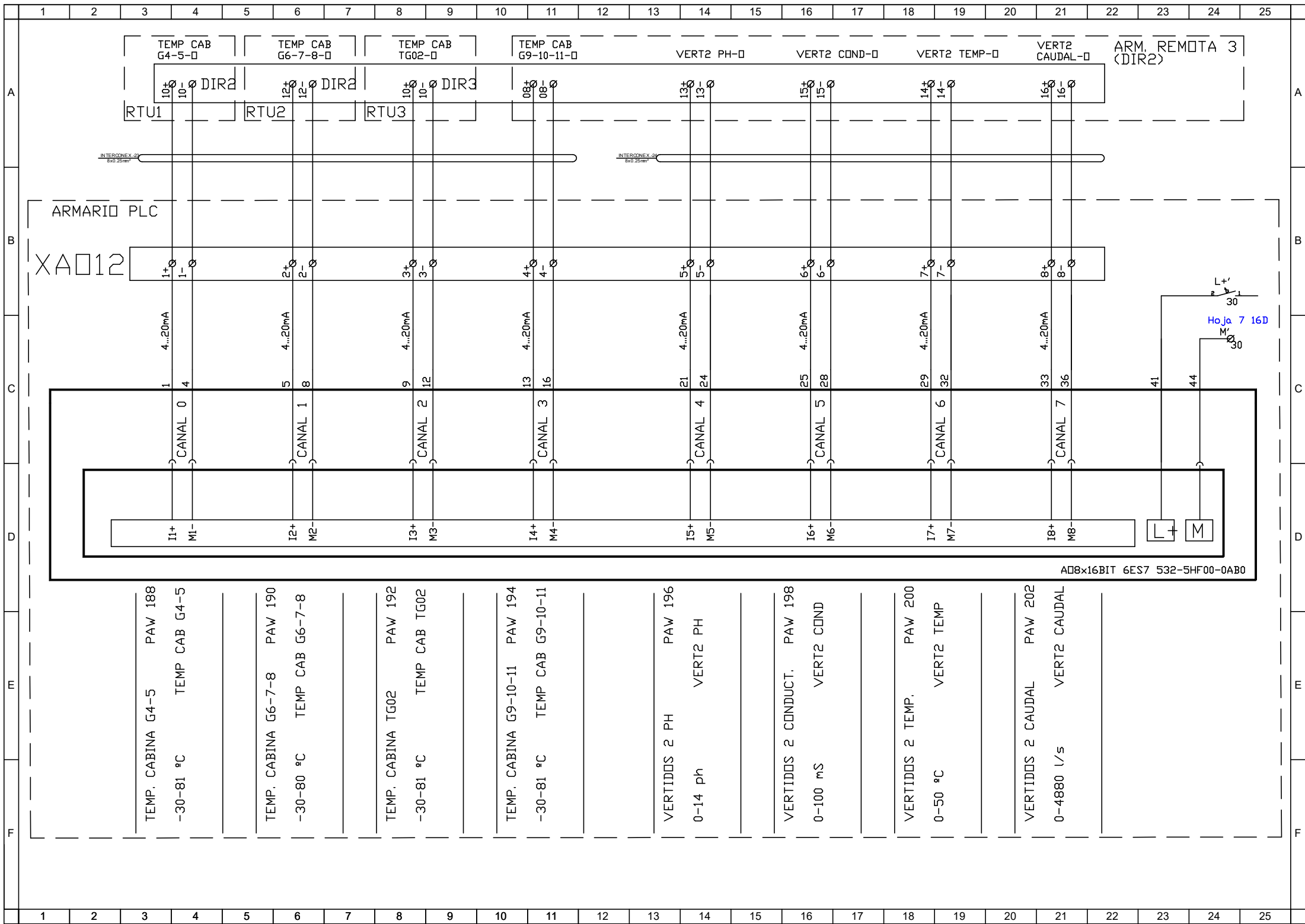
Hoja 7 3D

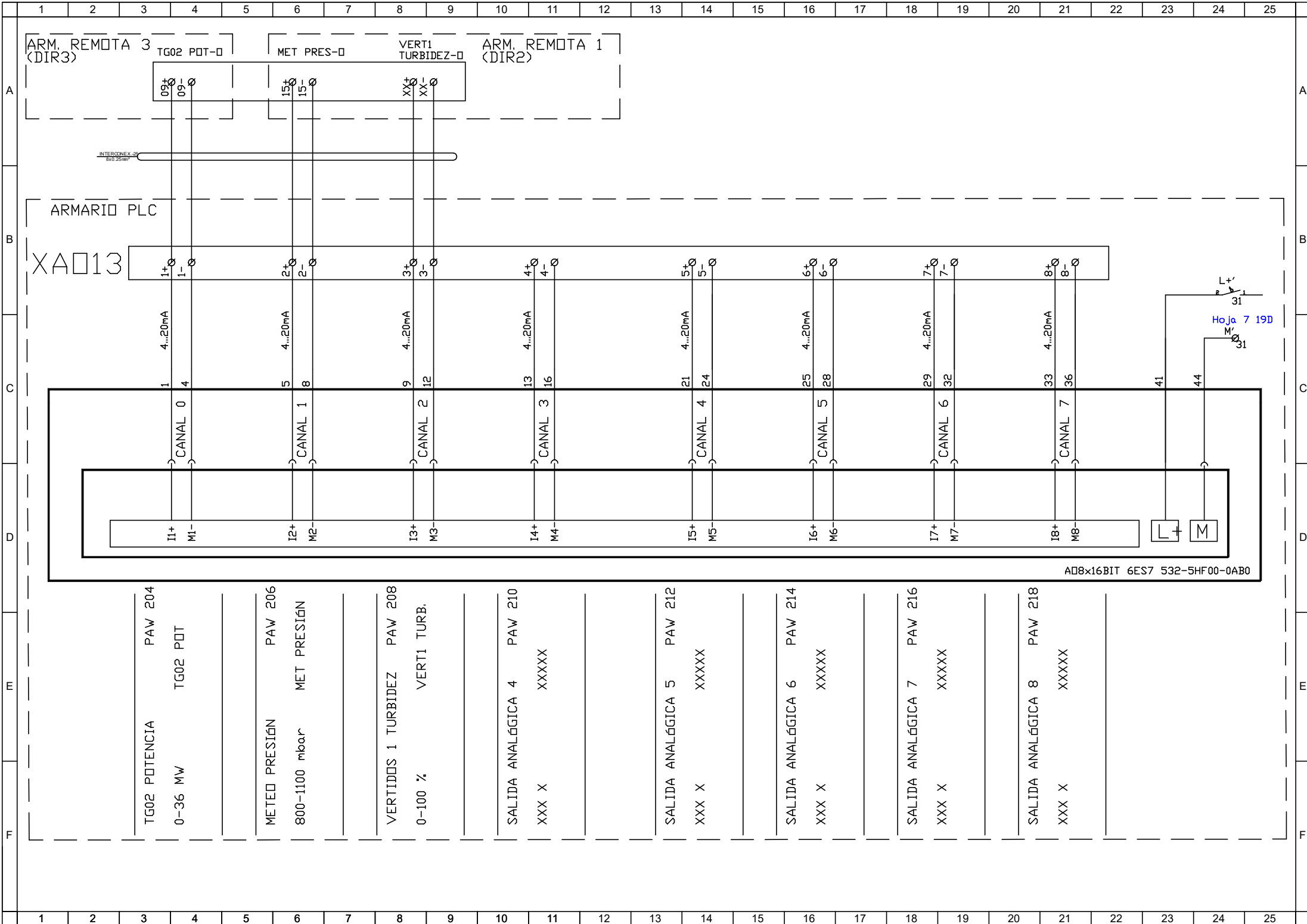


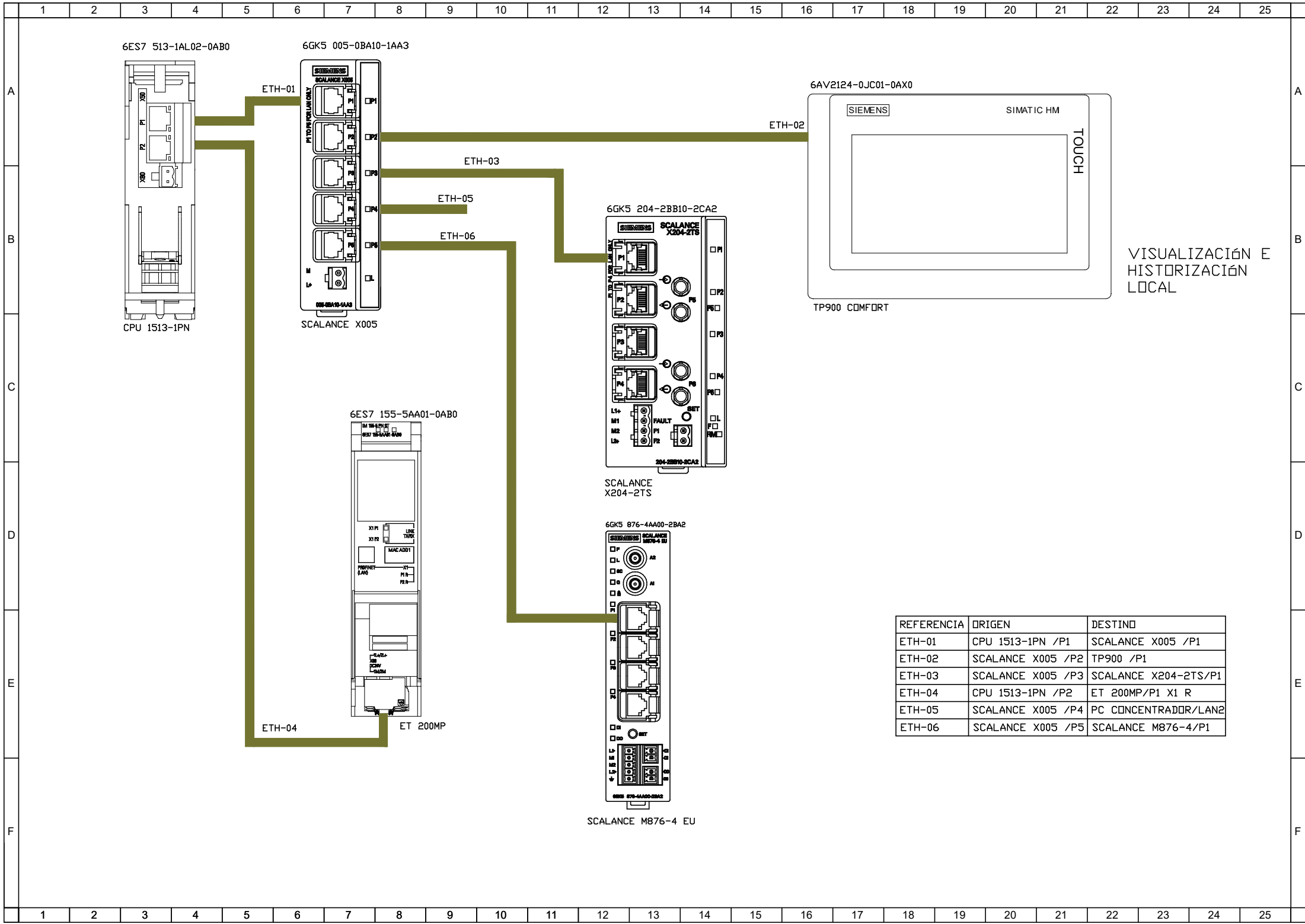












REFERENCIA	ORIGEN	DESTINO
ETH-01	CPU 1513-1PN /P1	SCALANCE X005 /P1
ETH-02	SCALANCE X005 /P2	TP900 /P1
ETH-03	SCALANCE X005 /P3	SCALANCE X204-2TS/P1
ETH-04	CPU 1513-1PN /P2	ET 200MP/P1 X1 R
ETH-05	SCALANCE X005 /P4	PC CONCENTRADOR/LAN2
ETH-06	SCALANCE X005 /P5	SCALANCE M876-4/P1